

Compal confidential

IGL50/51 Schematics Document

Mobile Yonah uFCPGA with Intel
Calistoga_GM/PM+ICH7-M core logic

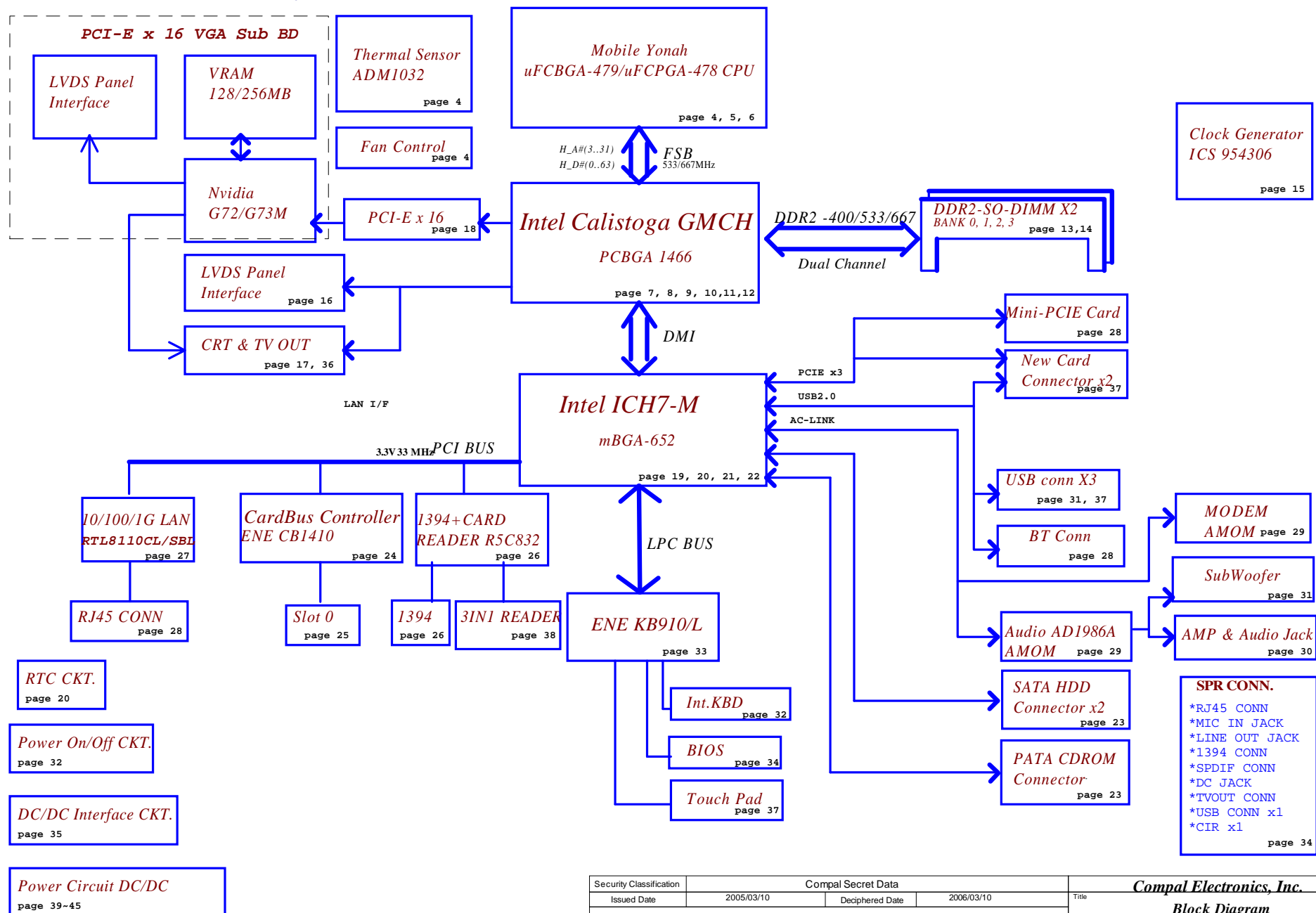
2006-07-31

REV:0.2

Security Classification		Compal Secret Data		Title	
Issued Date		2005/03/10	Deciphered Date	2006/03/10	Cover Sheet
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		Date:		星期二, 七月 31, 2006	Sheet 1 of 48

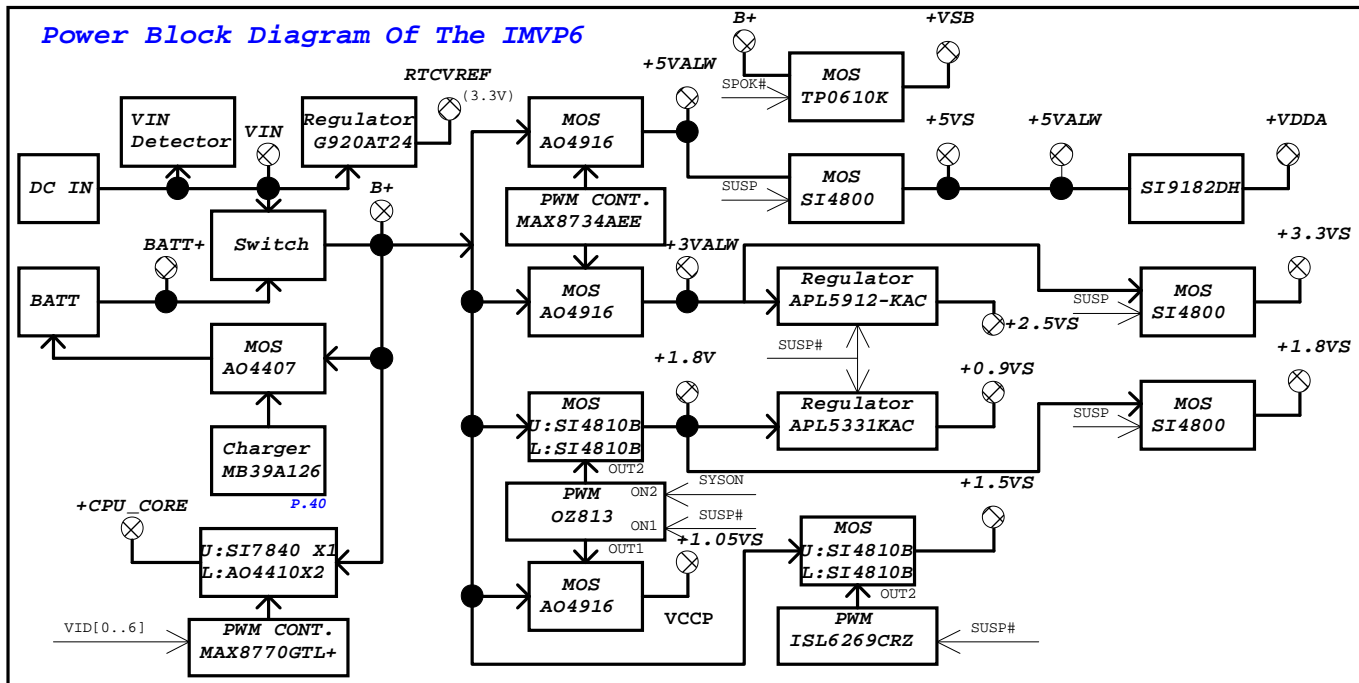
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File Name : LA-3061



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				Date	Rev
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Power Block Diagram Of The IMVP6



PCI DEVICES

EXTERNAL	IDSEL#	REQ/GNT#	PIRQ
CARD BUS CB1410	AD20	2	PCI_PIRQA#
CARD READER & 1394 R5C832	AD22	0	PCI_PIRQ# PCI_PIRQH#
LAN CONTROLLER RTL8110SBL/CL	AD17	3	PCI_PIRQF#

PCIE LANE

LANE	DEVICE
1	Express Card
2	Mini Card

I2C / SMB Address

DEVICE	ADDRESS R/W
KB910/L (SM1-Pulled-Up 5V)	
AT24C16AN	A3/A2 H
SMART BATTERY	17/16 H
KB910/L (SM2-Pulled-Up 3.3V)	
ADM1032AR	99/98 H
G7xM (I2CC-Pulled-Up 3.3V)	
G781-1 (RESERVED)	9B/9A
ICH7M SM Bus	
ICS9LPR325AKLFT	D3/D2 H (3.3V)
DDR II DIMM0	A1/A0 H (3.3V)
DDR II DIMM1	A3/A2 H (3.3V)
Express Card	NC (2.5V)
Mini-Express	NC (2.5V)

USB

PORT	DEVICE
0	LEFT SIDE
1	BLUE TOOTH
2	RIGHT SIDE
3	JP810
4	RIGHT SIDE
5	CMOS
6	RIGHT SIDE

BOM Structure

MARK	FUNCTION
@	NC FOR ALL
EXP@	PCIE-NEW CARD
BT@	BLUE TOOTH
UMA@	Internal 945GM
VGA@	External G7xM
SUBWOOFER@	SUBWOOFER
HGT30@	HGT30
CB@	PCMCIA/CARD BUS
GIGA@	8110SBL (SCL) Giga LAN
10/100@	8110CL 10/100Mb LAN

Voltage Rails

power plane	+B LDO3 LDO5	+5VALW +3VALW	+1.8V +5V	+5VS +3VS +2.5VS +1.8VS +1.5VS +VGA CORE +1.2VS +0.9VS +CPU CORE +VCCP
State S4 : STD S5 : SOFT OFF				
S0	O	O	O	O
S1	O	O	O	O
S3 : STR	O	O	O	X
S5 S4/AC	O	O	X	X
S5 S4/ Battery only	O	X	X	X
S5 S4/AC & Battery don't exist	X	X	X	X

MB_ID

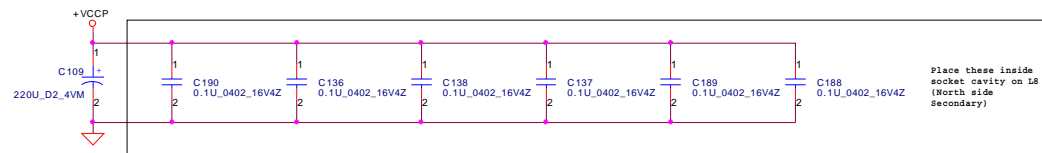
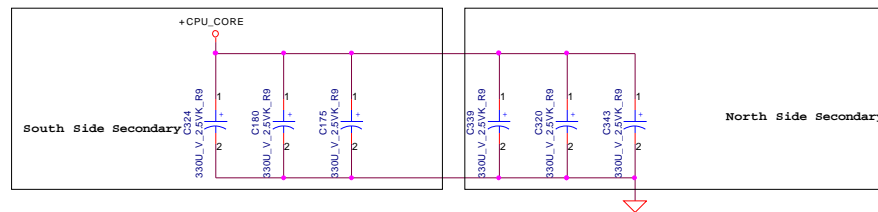
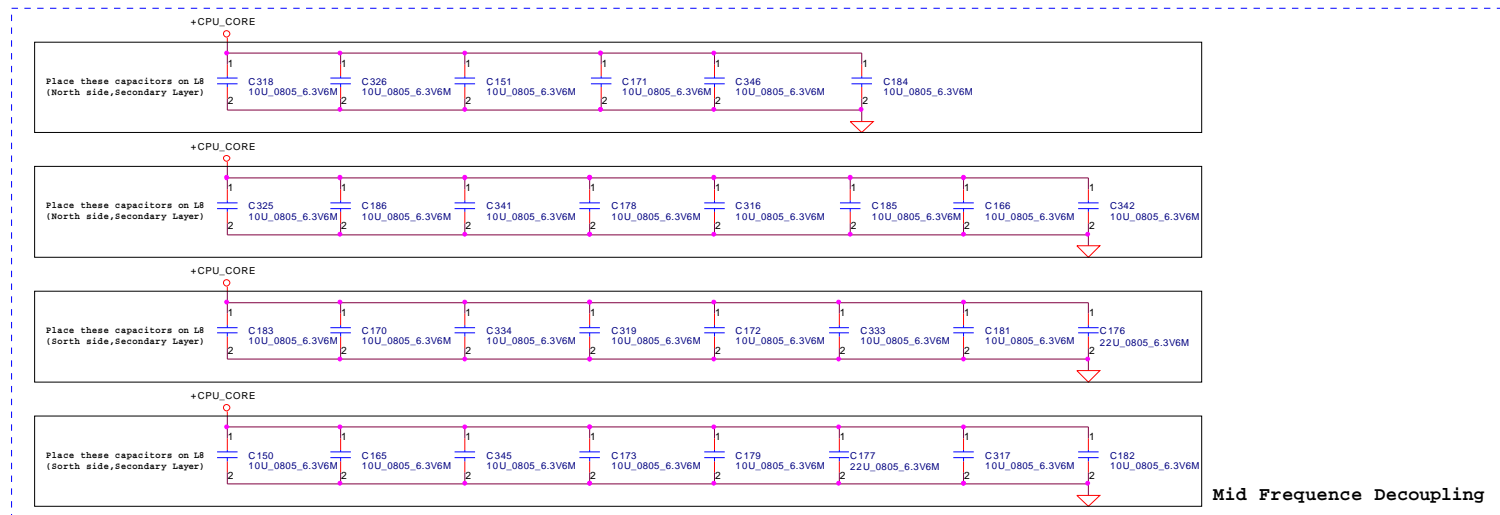
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1	IGL-51

BRD_ID

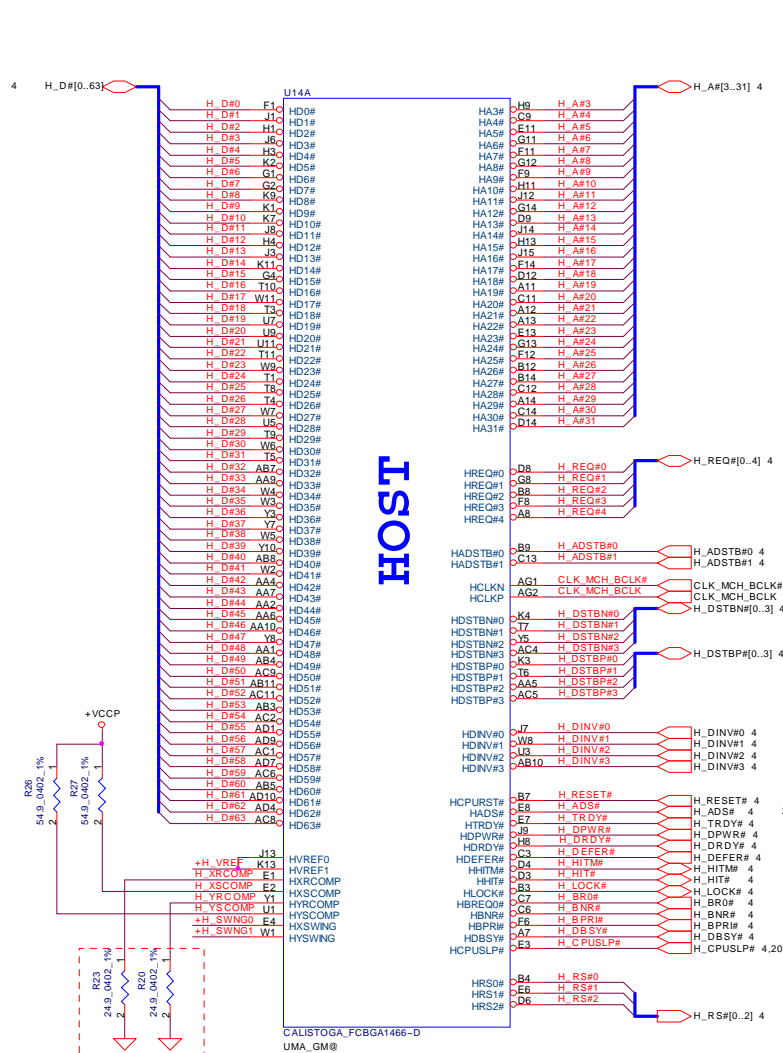
ID	MB REV#	R115(Rb)	Vab
0	R0.1 (EVT)	0	0V
1	R0.2 (DVT)	8.2K	0.25V
2	R0.3 (PVT)	18K	0.50V
3	R1.0 (MP)	33K	0.82V
4		56K	1.19V
5		100K	1.65V
6		200K	2.20V
7		NC	3.30V

+3VALW	KB910L SB RTL8110SBL/CL	mA 160mA
+CPU_CORE	CPU	36A
+VCCP	CPU NB	2.5A 9.8A (14.7A)
+5VS	EXPRESS CARD HDD ODD MDC APA2066 TPA0211 AD1986 USB PORT * 6	1A 1.5A 1.8A 300mA 1A mA 70mA 3A
+3VS	NB EXPRESS CARD CLK_GEN LCDVCC VGA CARD (G7XM) SB R5C832 BIOS ROM KB910L CB1410	480mA 1A 200mA 1A 655mA 680mA mA 15mA 200mA mA
+2.5VS	VGA CARD (G7XM) NB	130mA (143mA)
+1.8V	DDR2_DIMM NB (667Mhz)	8A 3.1A
+1.8VS	GDDR2 VGA CARD (G7XM)	6A 4.06A
+0.9VREF	DDR2_DIMM	10mA
+0.9VS	GDDR2 DDR2_DIMM	1A 2A
+1.5V	SB	40mA
+1.5VS	NB SB MiniCard EXPRESS CARD VGA CARD (G7XM)	8.9A (13.8A) 3.8A 1A 0.65A 2A

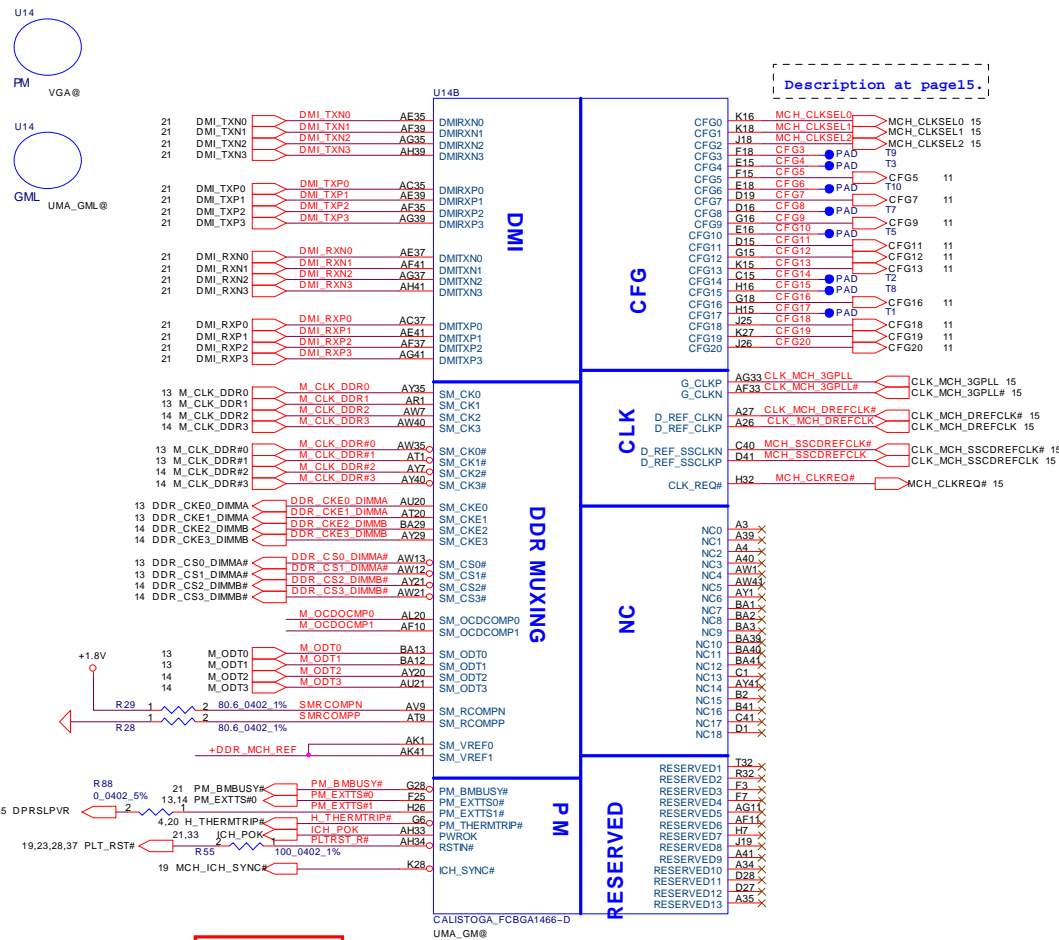
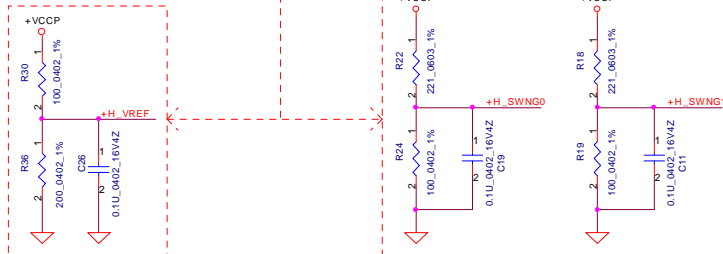
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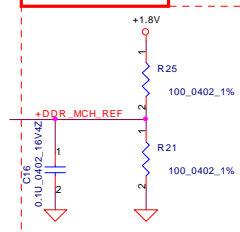
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				2006.07.31
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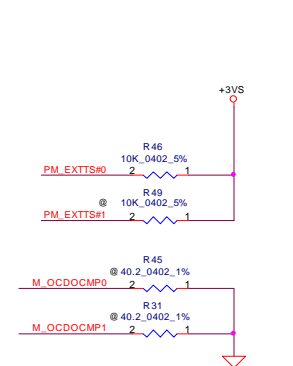
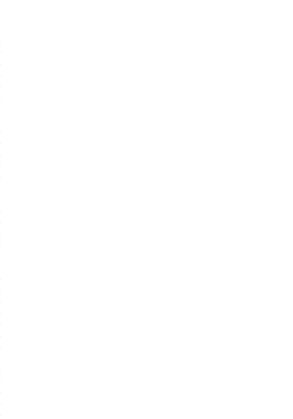
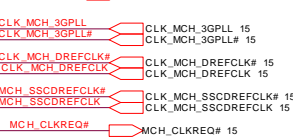
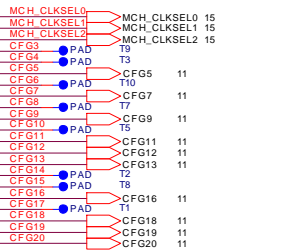
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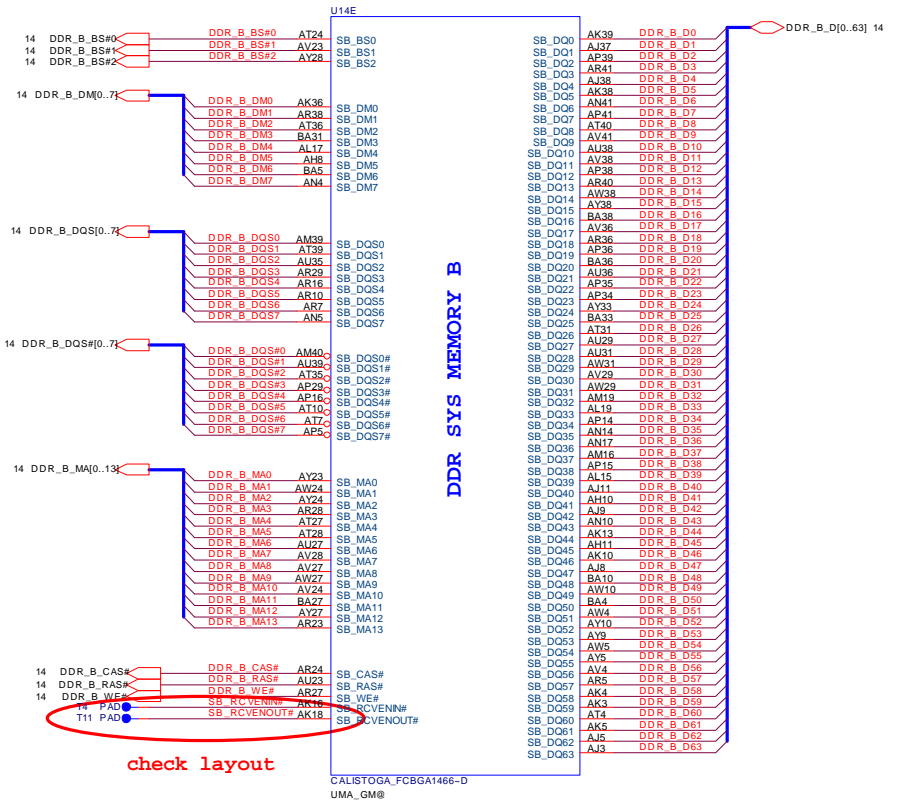
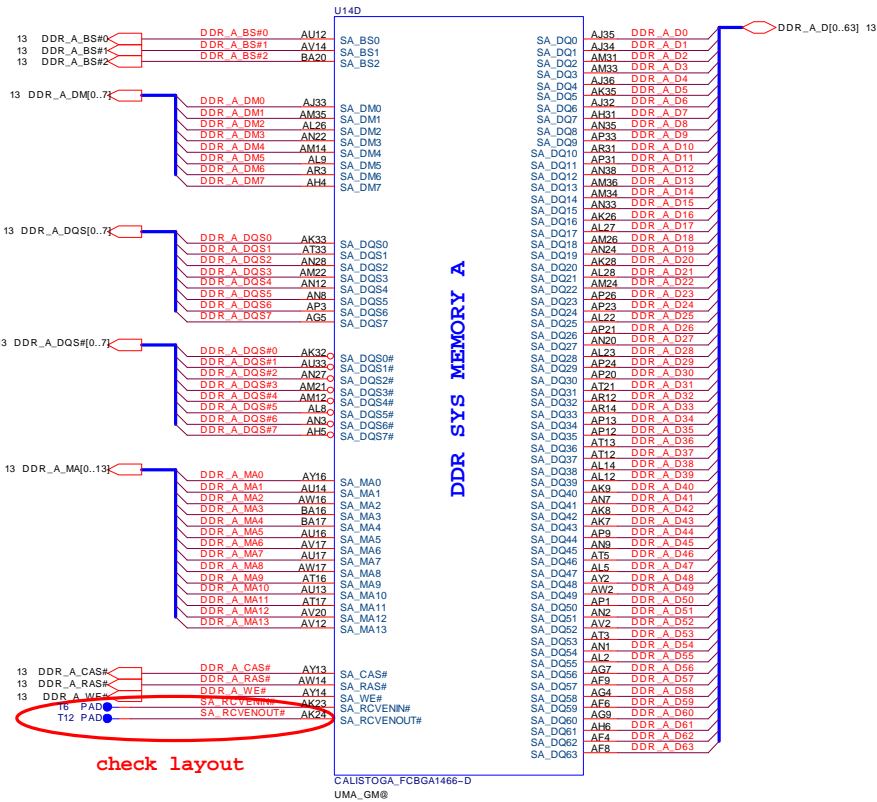
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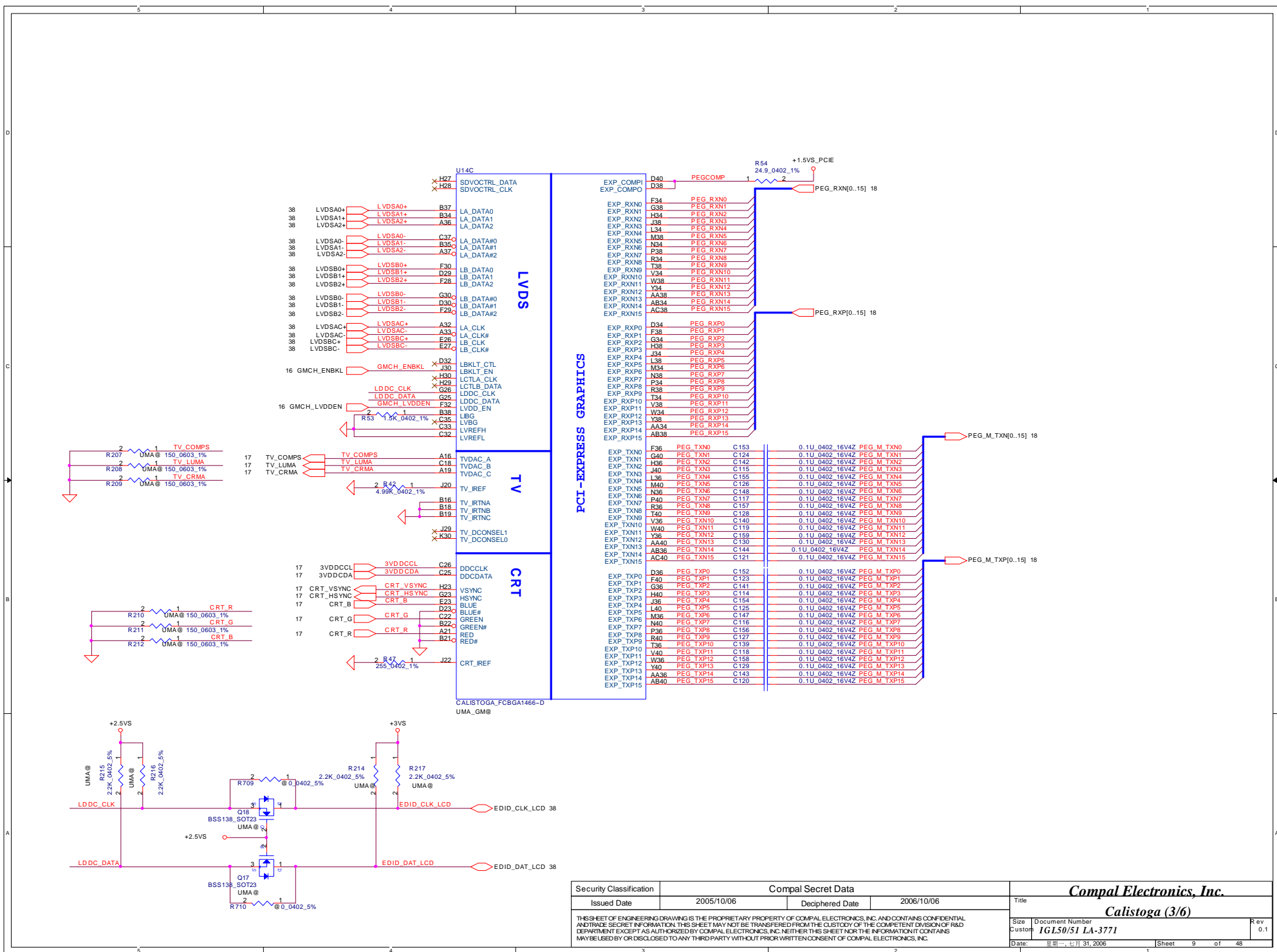
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Date		2006-11-30		Sheet		7 of 48	

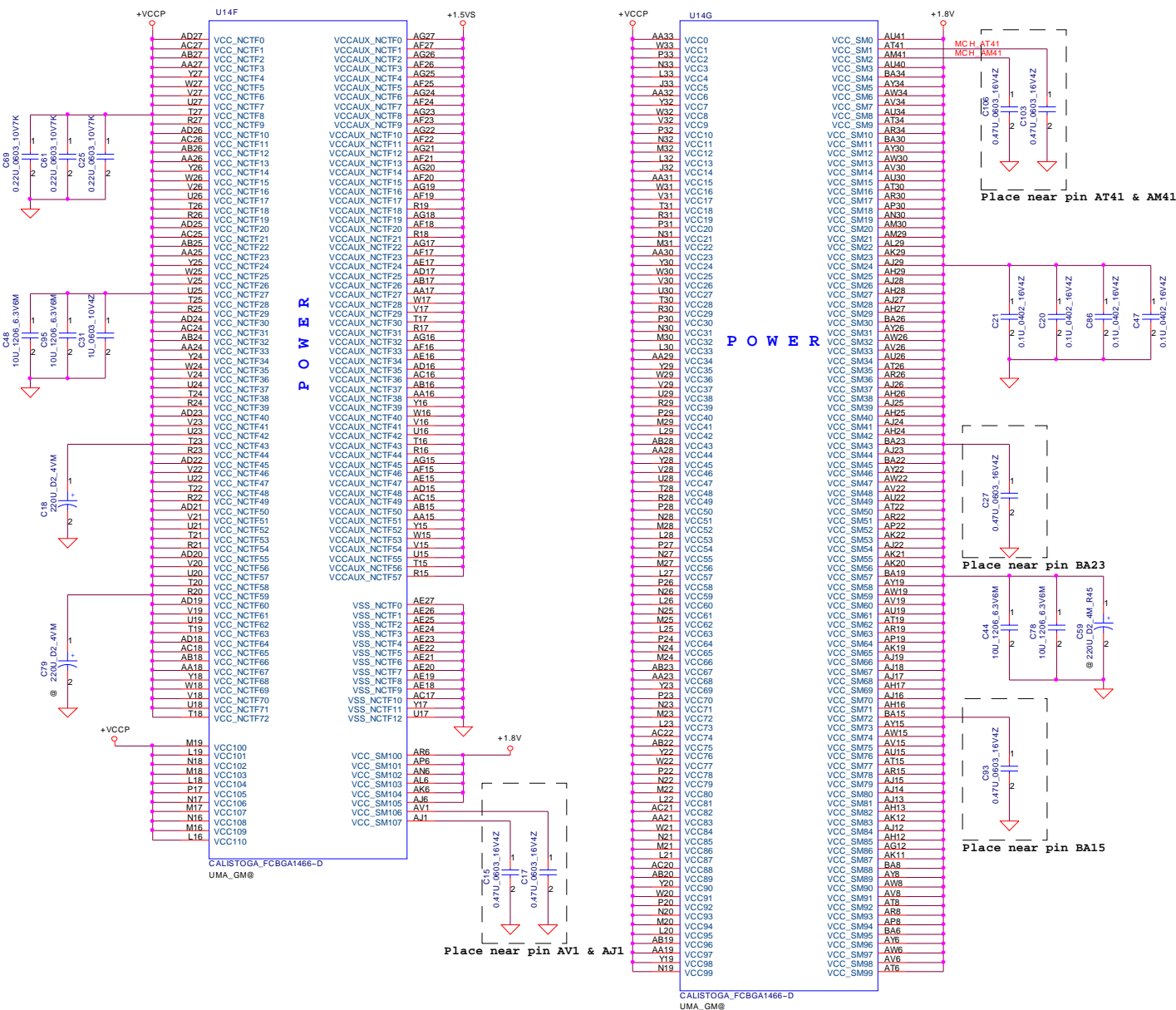
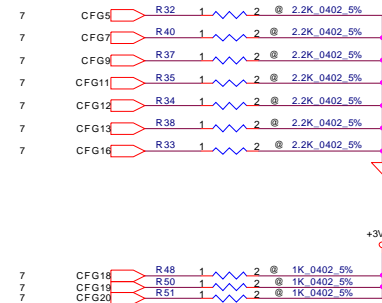


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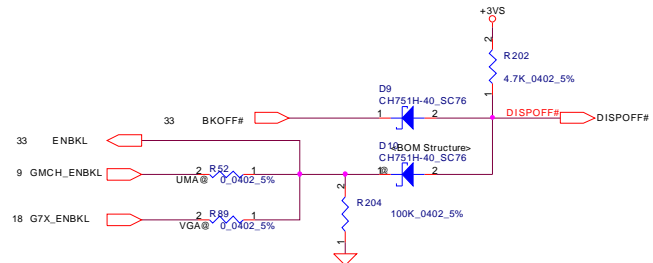
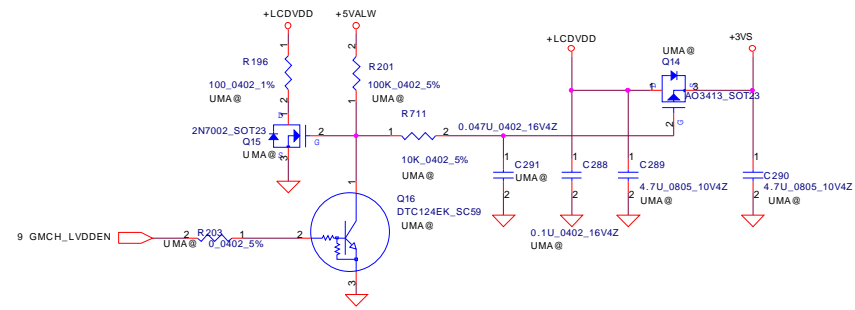
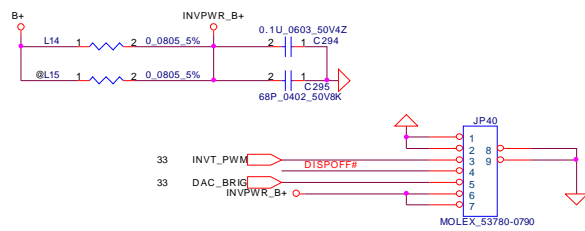

```
CFG[3:17] have internal pull up
CFG[19:18] have internal pull down
```

CFG[2:0]	011 = 657MT/s FSB 001 = 533MT/s FSB
CFG5	0 = DMI x 2 1 = DMI x 4 * (Default)
CFG7	0 = Reserved 1 = Mobile Yonah CPU * (Default)
CFG9	0 = Lane Reversal Enable 1 = Normal Operation (Default) *
CFG6	0 = Reserved
PSB 4X CLK Enable	1 = Calistoga *
CFG[13:12]	00 = Reserved 01 = XOR Mode Enabled 10 = All 2 Mode Enabled 11 = Normal Operation * (Default)
CFG16	0 = Dynamic ODT Disabled 1 = Dynamic ODT Enabled * (Default)
CFG10 CFG18	10 = 1.05V * (Default) 01 = 1.5V
CFG19	0 = Normal Operation * (Default) 1 = DMI Lane Reversal Enable
SDVO_CTRLDATA	0 = No SDVO Device Present * (Default) 1 = SDVO Device Present
CFG20 (PCIE/SDVO select)	0 = Only PCIE or SDVO is operational. * (Default) 1 = PCIE/SDVO are operating simul.



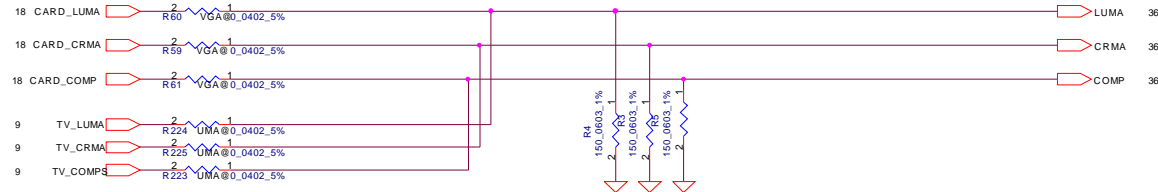
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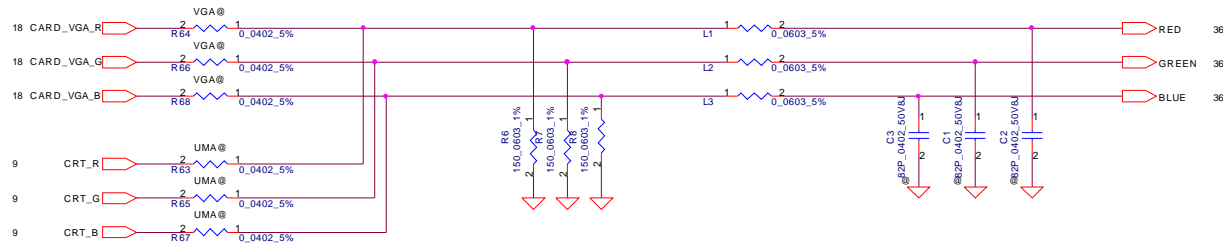
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TV-OUT Conn.

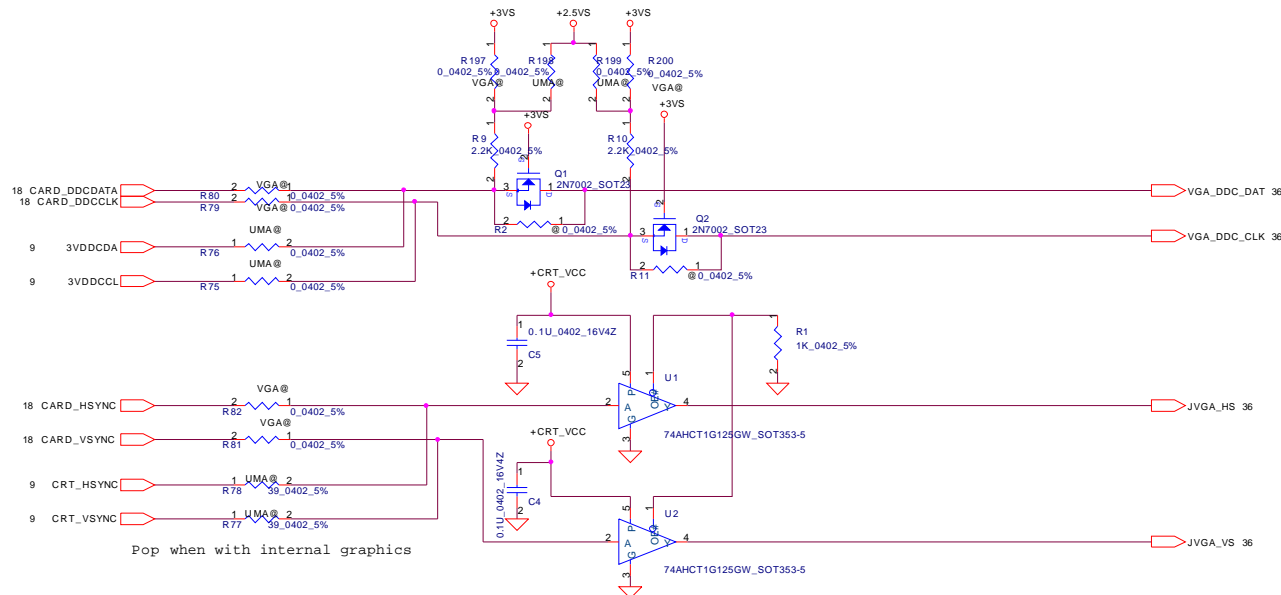


Pop when with internal graphics

CRT Conn.

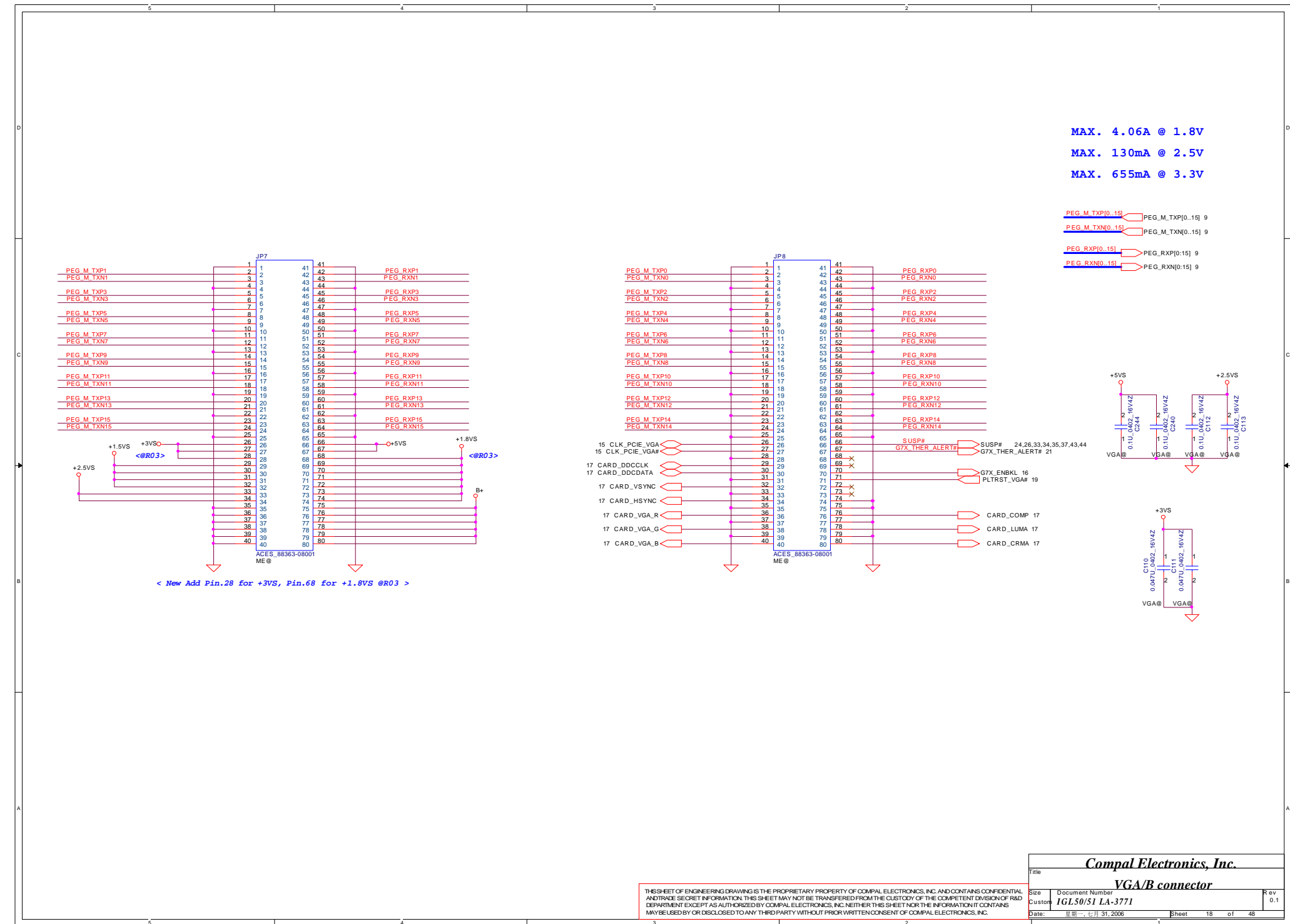


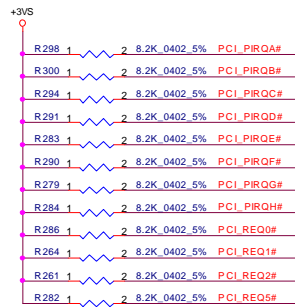
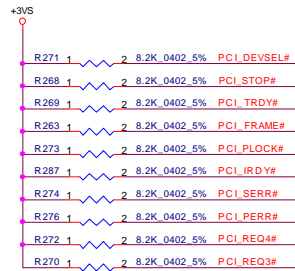
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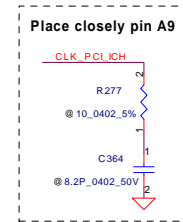
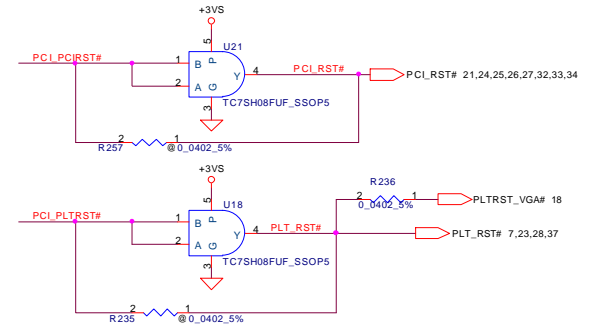
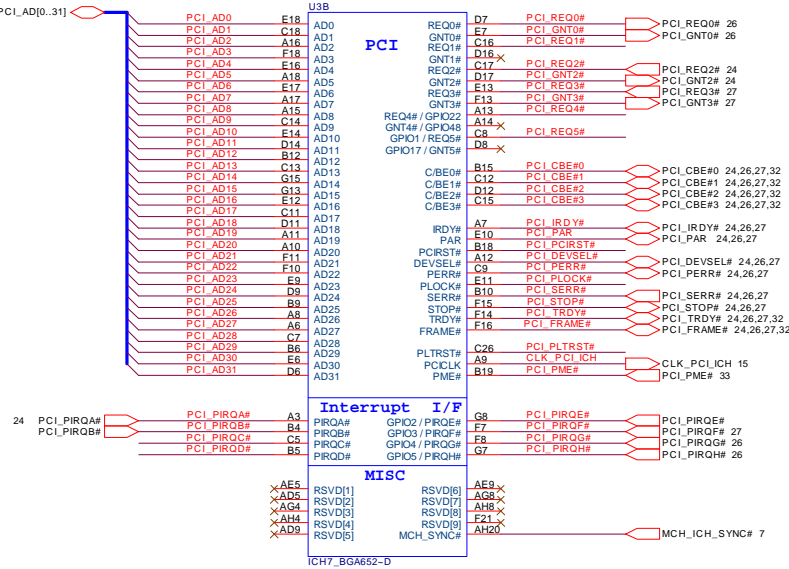
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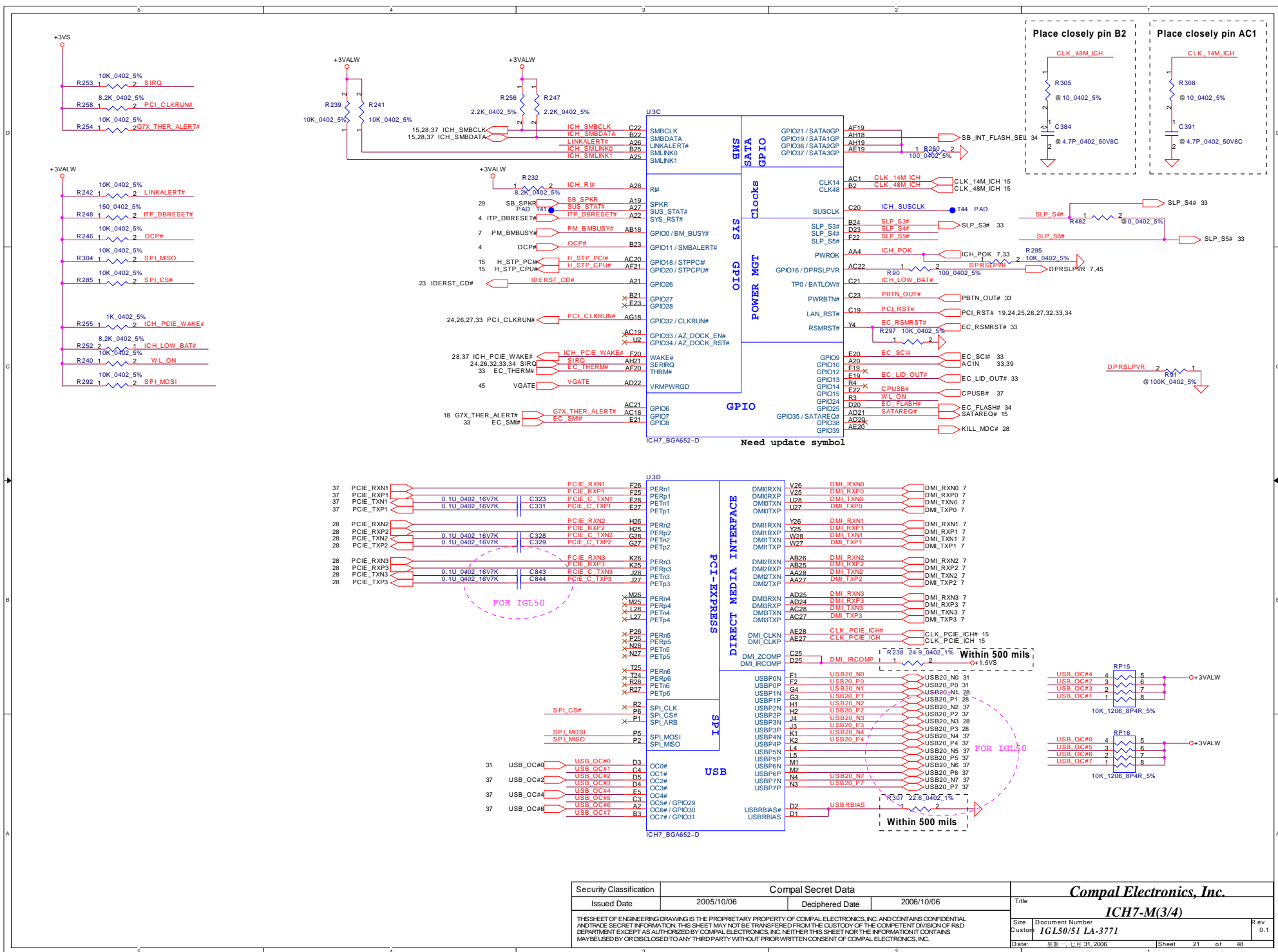




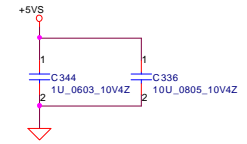
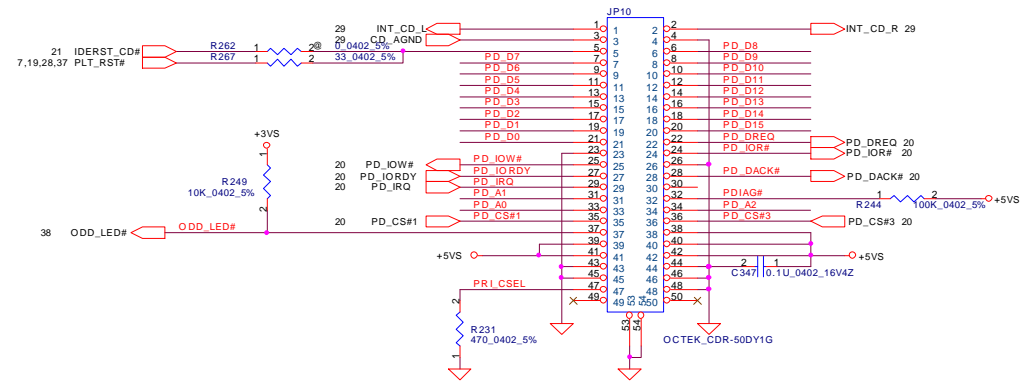
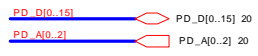
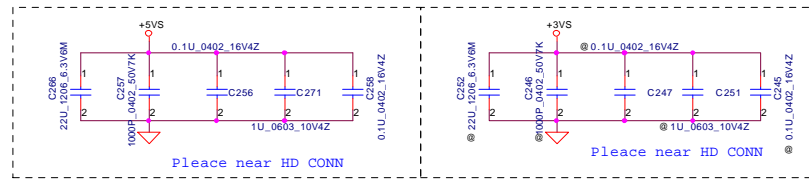
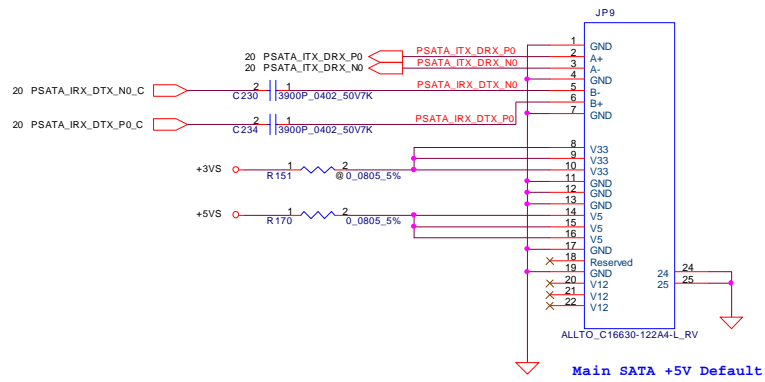
24,26,27,32 PCI_AD[0..31]



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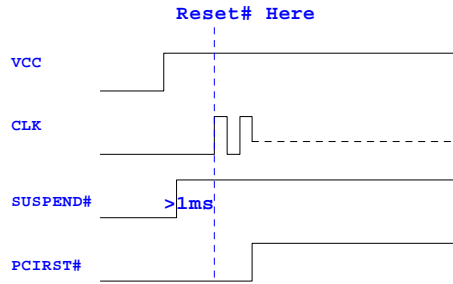


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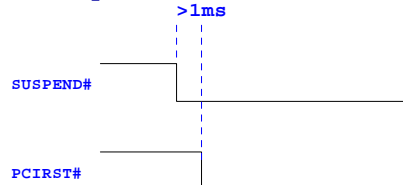


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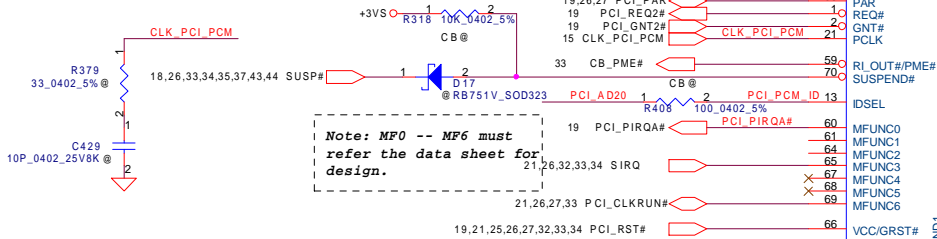
Power on RESET#



Entry S3



SUSPEND# will gate the PCIRST# or GRST#, so need S3 wake up function, SUSPEND# must be LOW ahead the PCIRST# about 1ms.



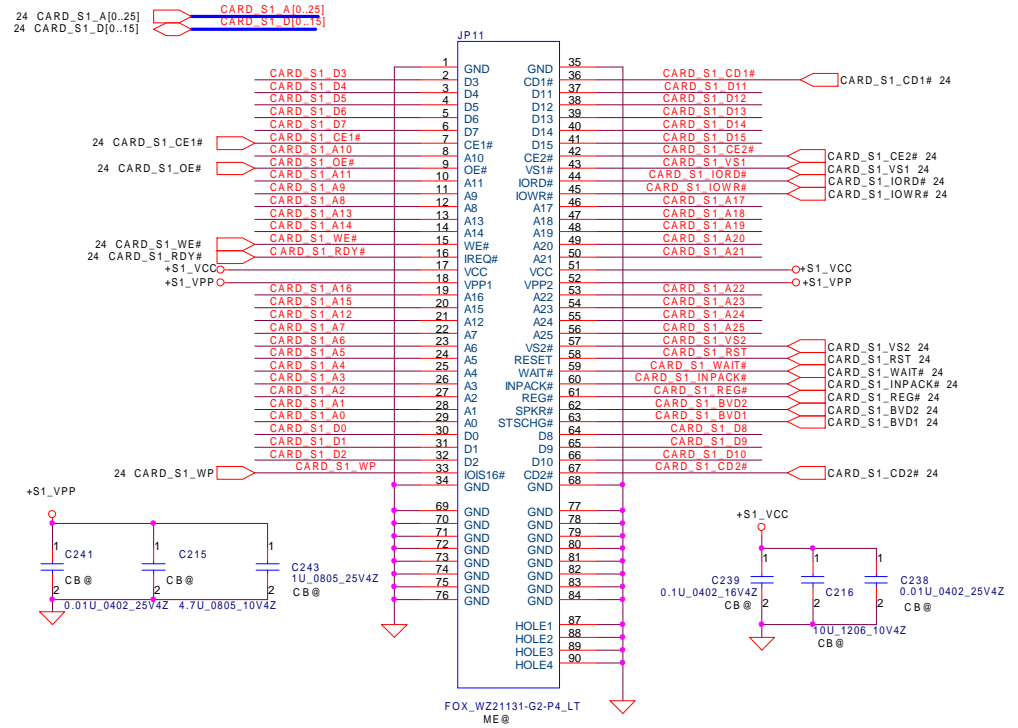
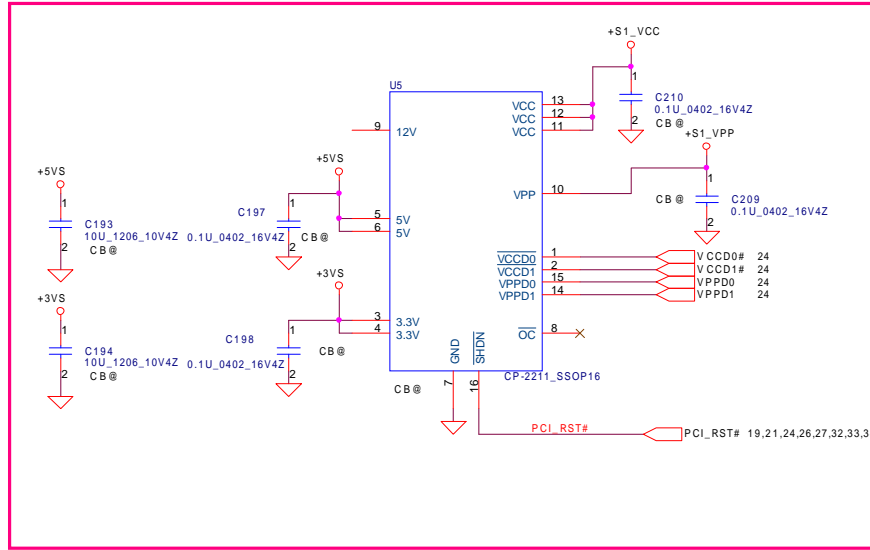
Note: MF0 -- MF6 must refer the data sheet for design.

PQFP 144
22.2 X 22.2 X 1.60

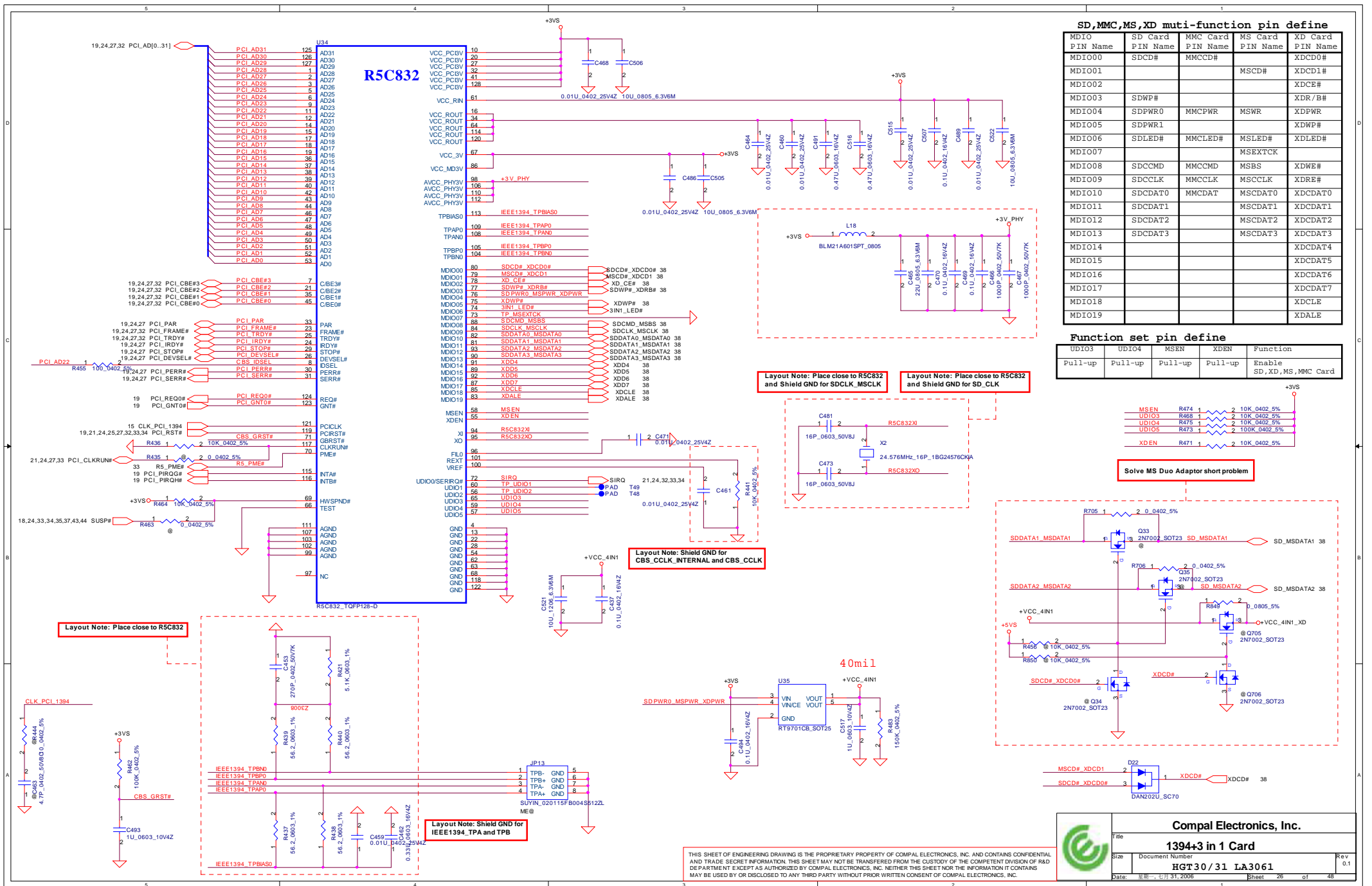
ENE CBI410 just have one vcc plane internal, if want S3 wake-up function(PME#), then at S3 status must keep all Vcc +3V. That is different with TI 1410 and O2-Micro 6912, just keep the VCCI pin +3V, the other vcc can use +3VS.

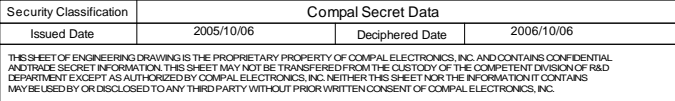
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/10/06	Deciphered Date	2006/10/06	Title	
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Date: 06/31/2006		Sheet 24 of 48		Rev 0.1	

PCMCIA Power Controller

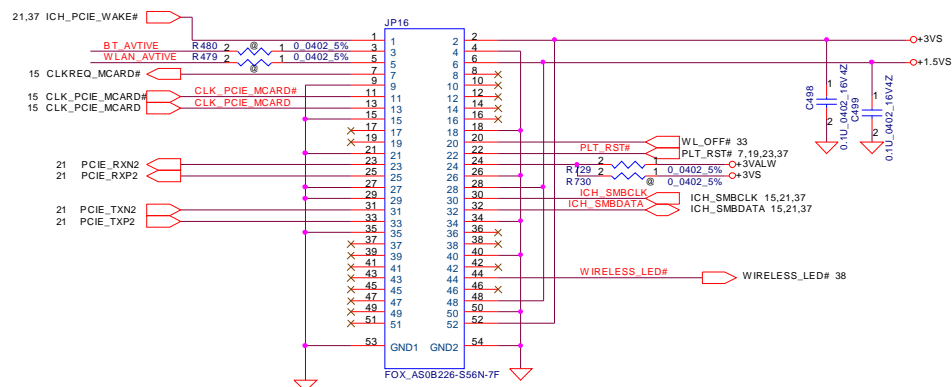


Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/10/06	Deciphered Date	2006/10/06	Title	
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				Size	Document Number
				Customer	IGL50/51 LA-3771
				Date	Rev
				2006-11-01	0.1
				Sheet	25 of 48

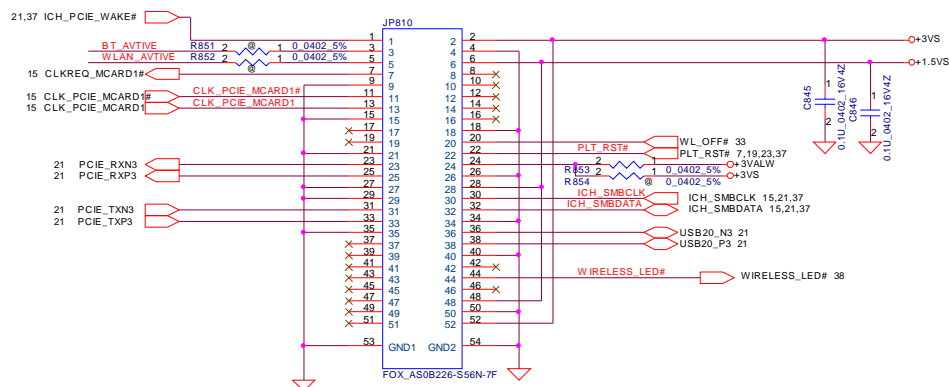




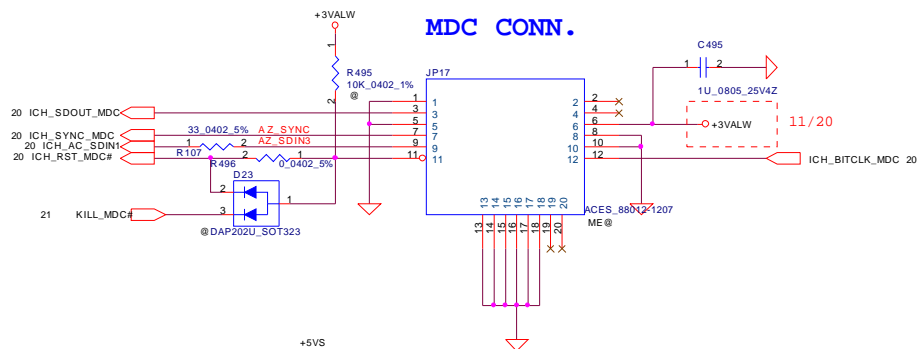
Mini-Express Card(Slot 1-WLAN)



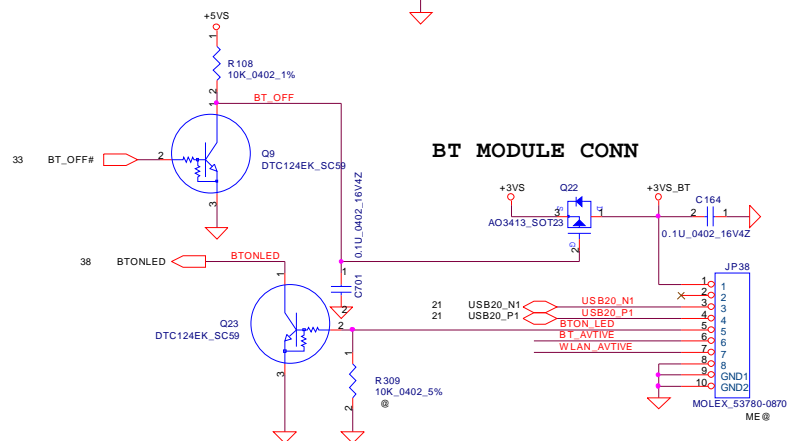
Mini-Express Card(Slot 2-WLAN)



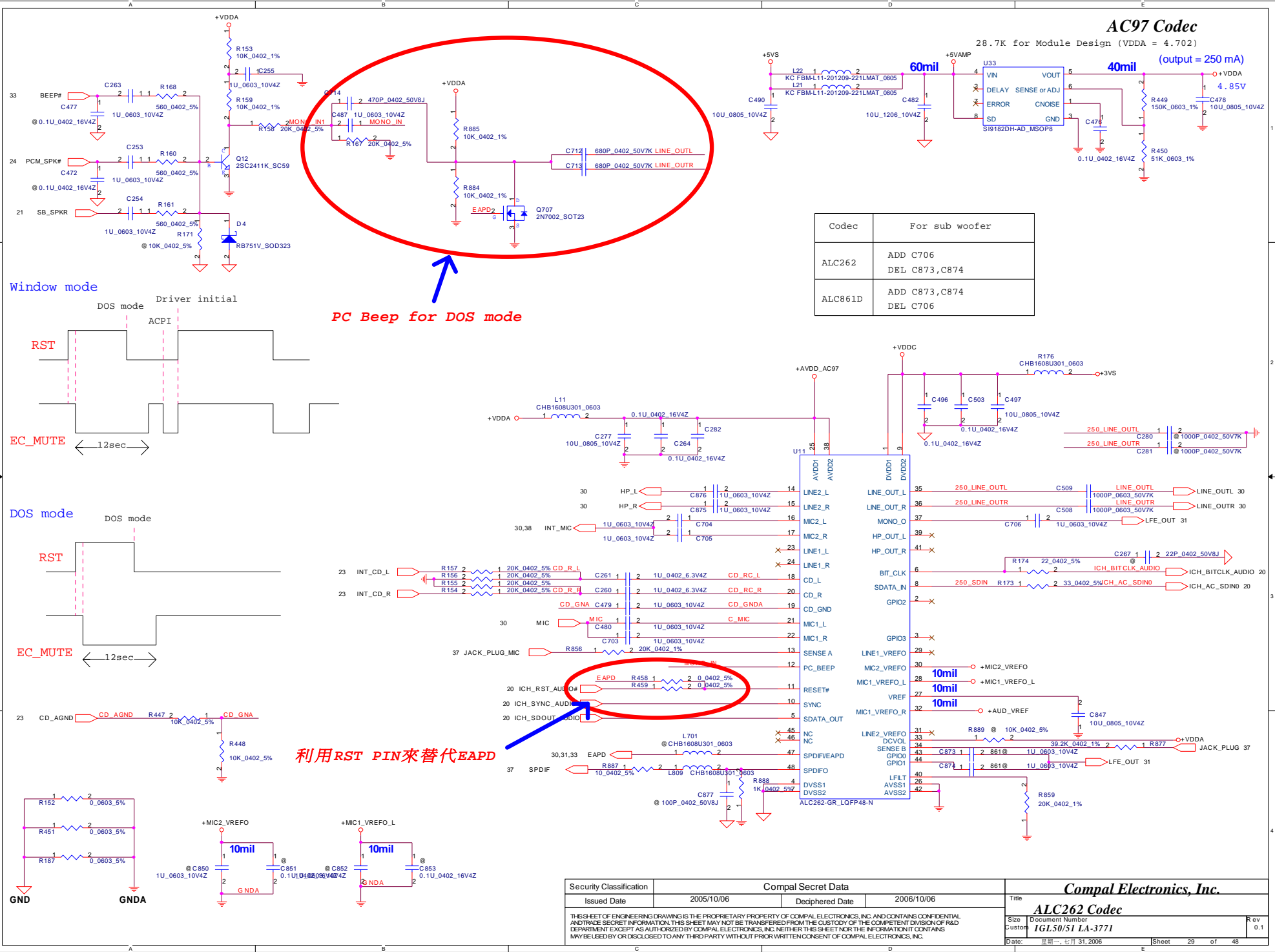
MDC CONN.

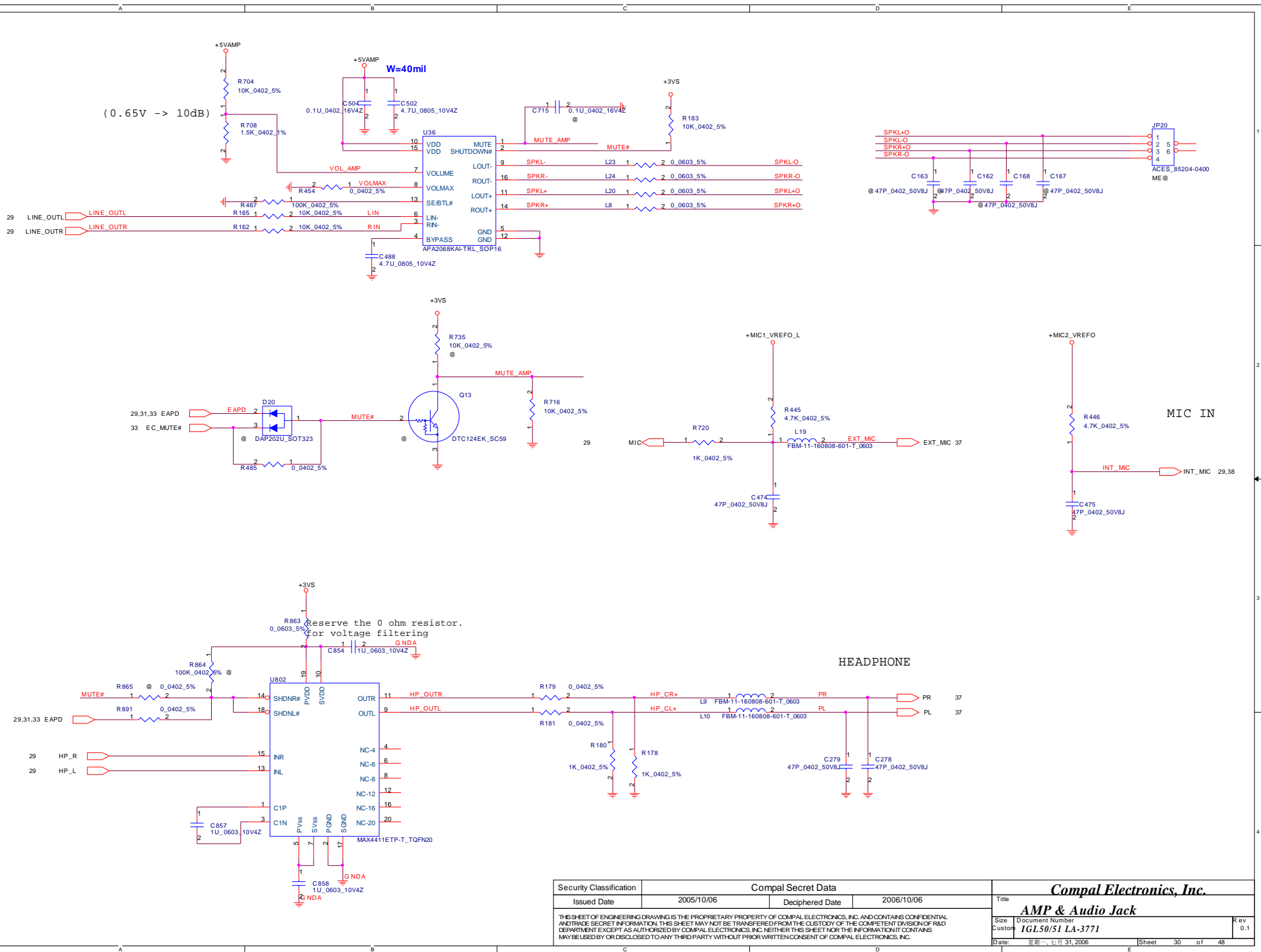


BT MODULE CONN

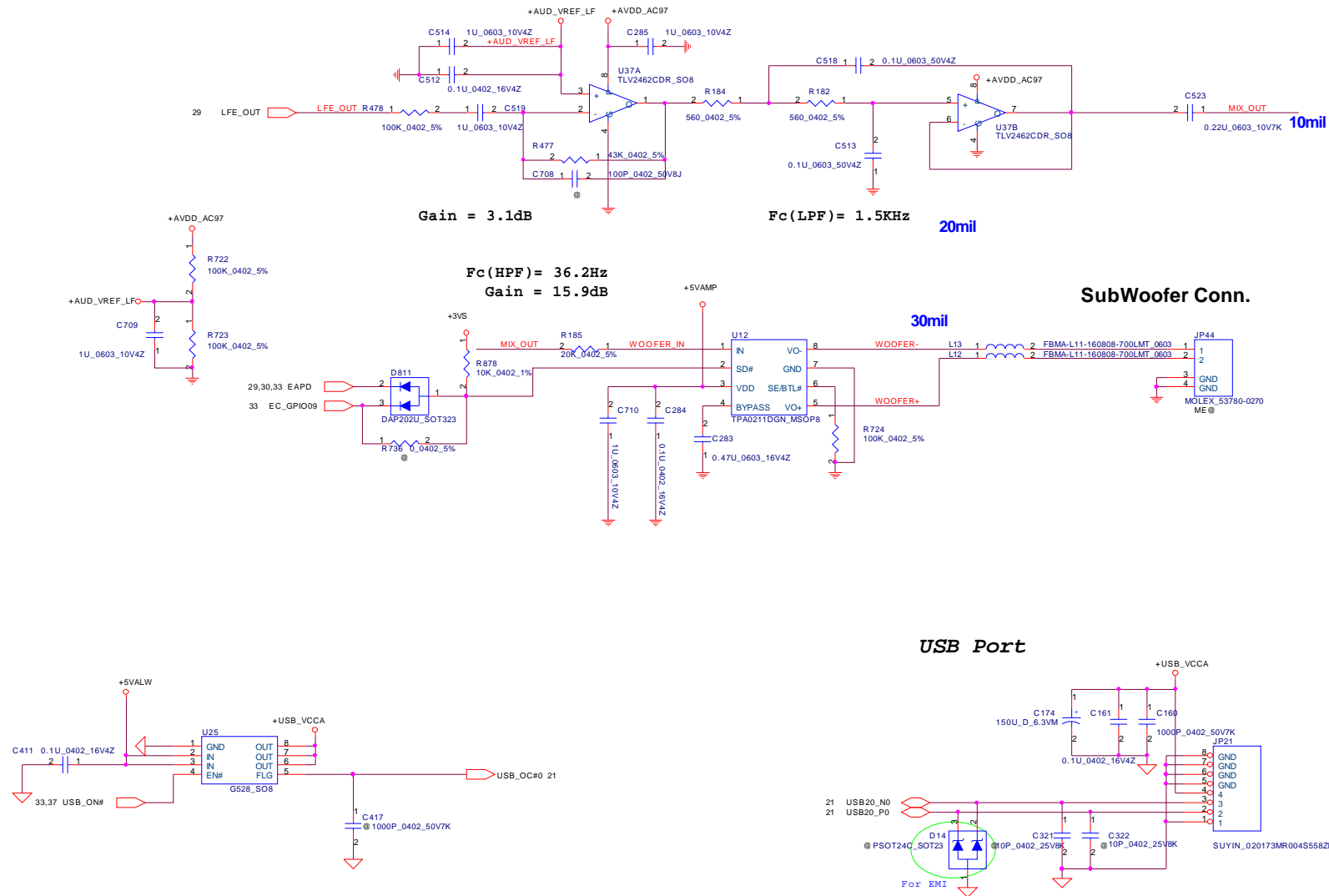


Security Classification		Compal Secret Data		Compal Electronics, Inc. Mini Card / MDC CONN	
Issued Date	2005/10/06	Deciphered Date	2006/10/06	Title	
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				Custom	0.
				Document Number 1GL50/51 LA-3771	
Date:		星期五, 七月 31, 2006 Sheet 28 of 48			

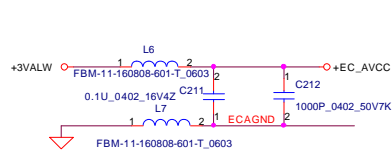




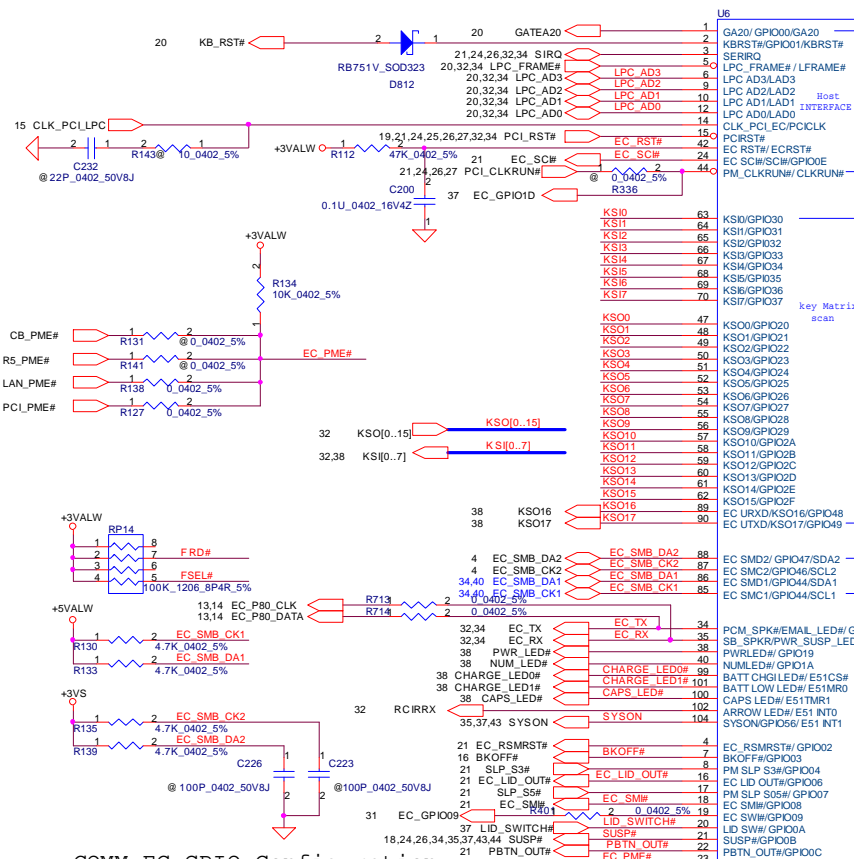
WOOFER@



Security Classification		Compal Secret Data		Compal Electronics, Inc. Bluetooth & USB CONN.	
Issued Date	2005/10/06	Deciphered Date	2006/10/06	Title	
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				Date:	第 31 页 共 48 页 2006/01/01

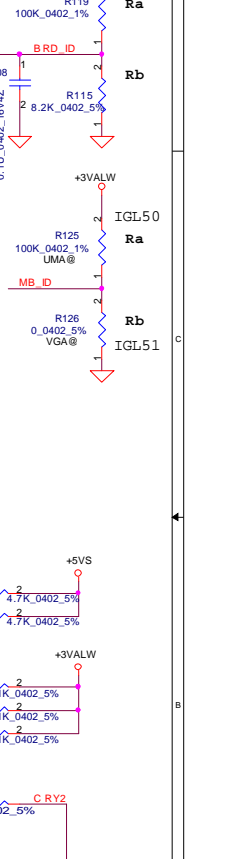
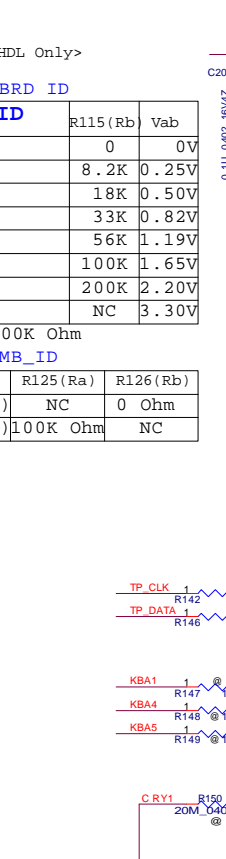
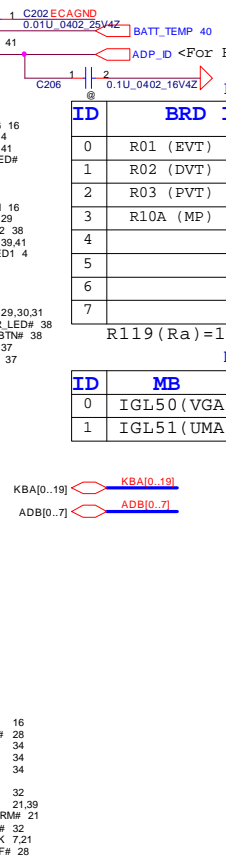
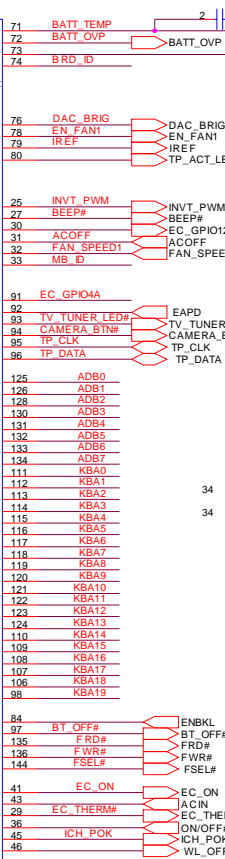
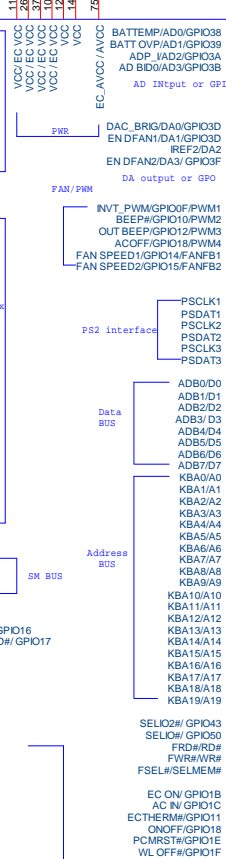


Analog Board ID definition,
Please see page 3.



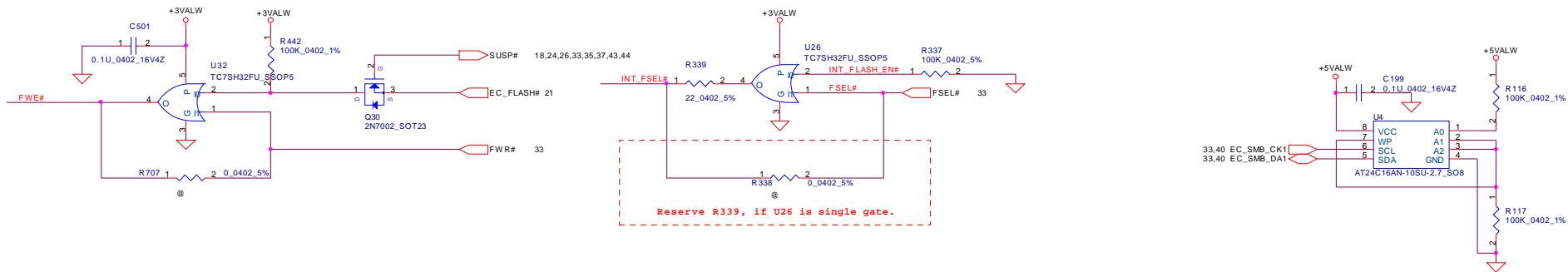
COMM EC_GPIO Configuration

PIN	GPIO	HDL00/HDL10	HGT30/HGT31	IGL50/IGL51
80	EC_GPIO3F	NC	TP_ACT_LED#	NC
19	EC_GPIO09	EASY_KEY1#	LED6#	SUBWOOFER_MUTE#
35	EC_GPIO17	SUSP_LED#	EC_P80_DATA	EC_P80_DATA
44	EC_GPIO1D	NC	LED3#	TP_ACT_LED#
91	EC_GPIO4A	NC	LED4#	USB_ON#
92	EC_GPIO4B	NC	EAPD IN	EAPD IN
93	EC_GPIO4C	NC	LED1#	TV_TUNER_LED#
94	EC_GPIO4D	NC	LED2#	CAMERA_BTN#
102	EC_GPIO55	NC	LED5#	RCIRRX
97	EC_GPIO50	MEDIA#	BT_OFF#	BT_OFF#
137	EC_GPIO57	VOL_UP#	NOVO_BTN#	NOVO_BTN#
142	EC_GPIO58	VOL_DOWN#	SLP_S4#	SLP_S4#
143	EC_GPIO59	KILL_SW#	WIRE_LAN_BTN#	RF_BTN#
30	EC_GPIO12	AMP_MUTE	TP_LOCK_LED#	SUBWOOFER_BTN#



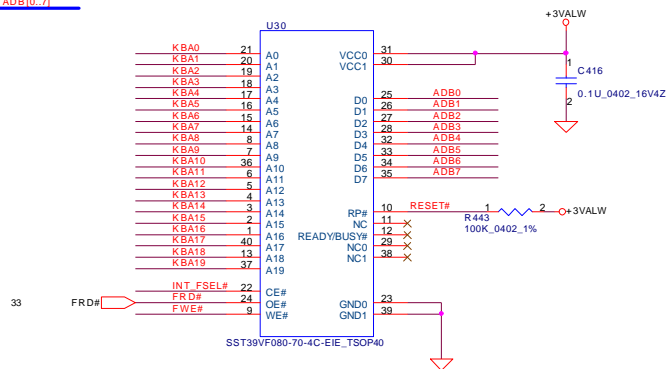
ID	BRD ID	R115 (Rb)	Vab
0	R01 (EVT)	0	0V
1	R02 (DVT)	8.2K	0.25V
2	R03 (PVT)	18K	0.50V
3	R10A (MP)	33K	0.82V
4		56K	1.19V
5		100K	1.65V
6		200K	2.20V
7		NC	3.30V

ID	MB	R125 (Ra)	R126 (Rb)
0	IGL50 (VGA)	NC	0 Ohm
1	IGL51 (UMA)	100K Ohm	NC

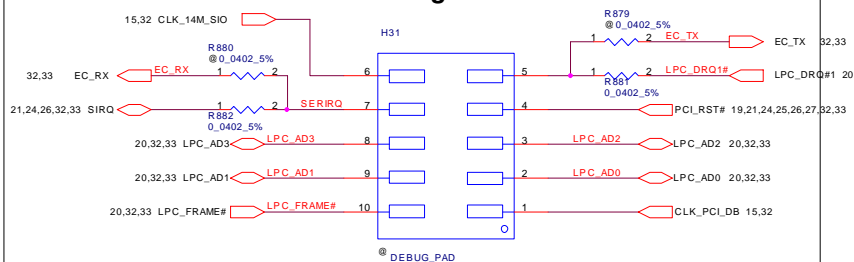


33 KBA[0..19] KBA[0..19]
33 ADB[0..7] ADB[0..7]

1MB Flash ROM



New LPC Debug Pad ---- MB side



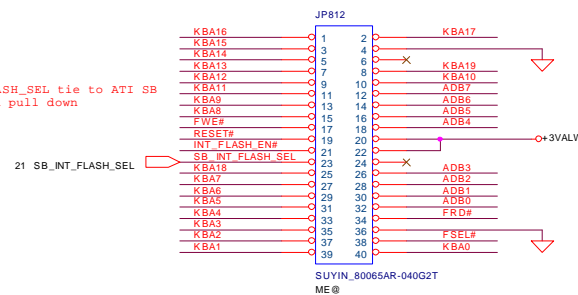
Under DDR ME Assignment Area

Keep Resistor near Debug Pad and in the same side

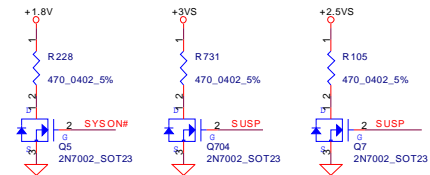
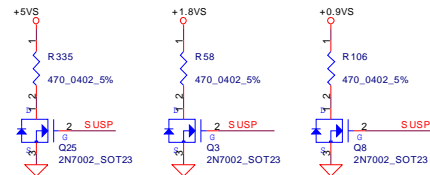
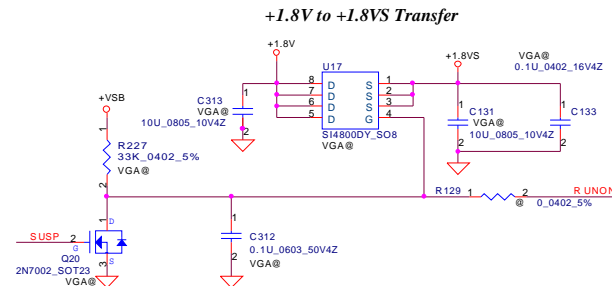
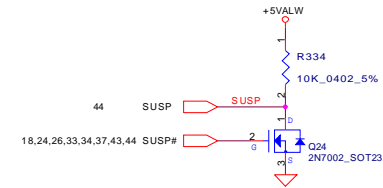
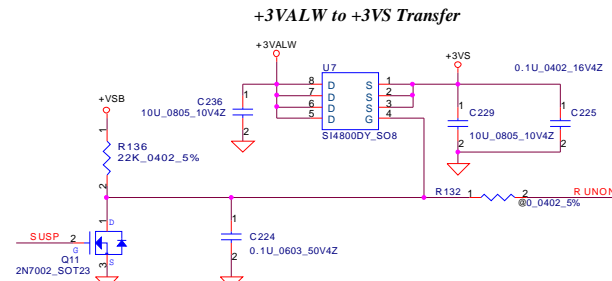
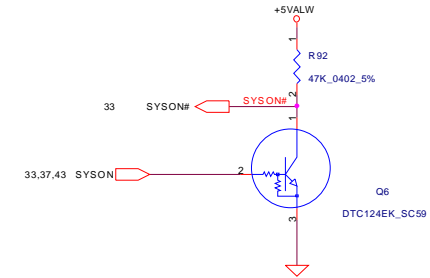
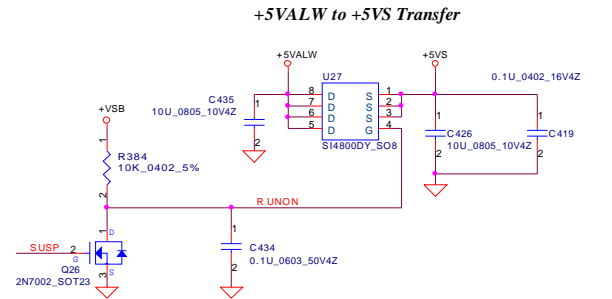
Standard side DIMM ---- Pin 1 near DIMM

Reverse side DIMM ---- Pin 1 keep away DIMM

Change to small socket for ROM part

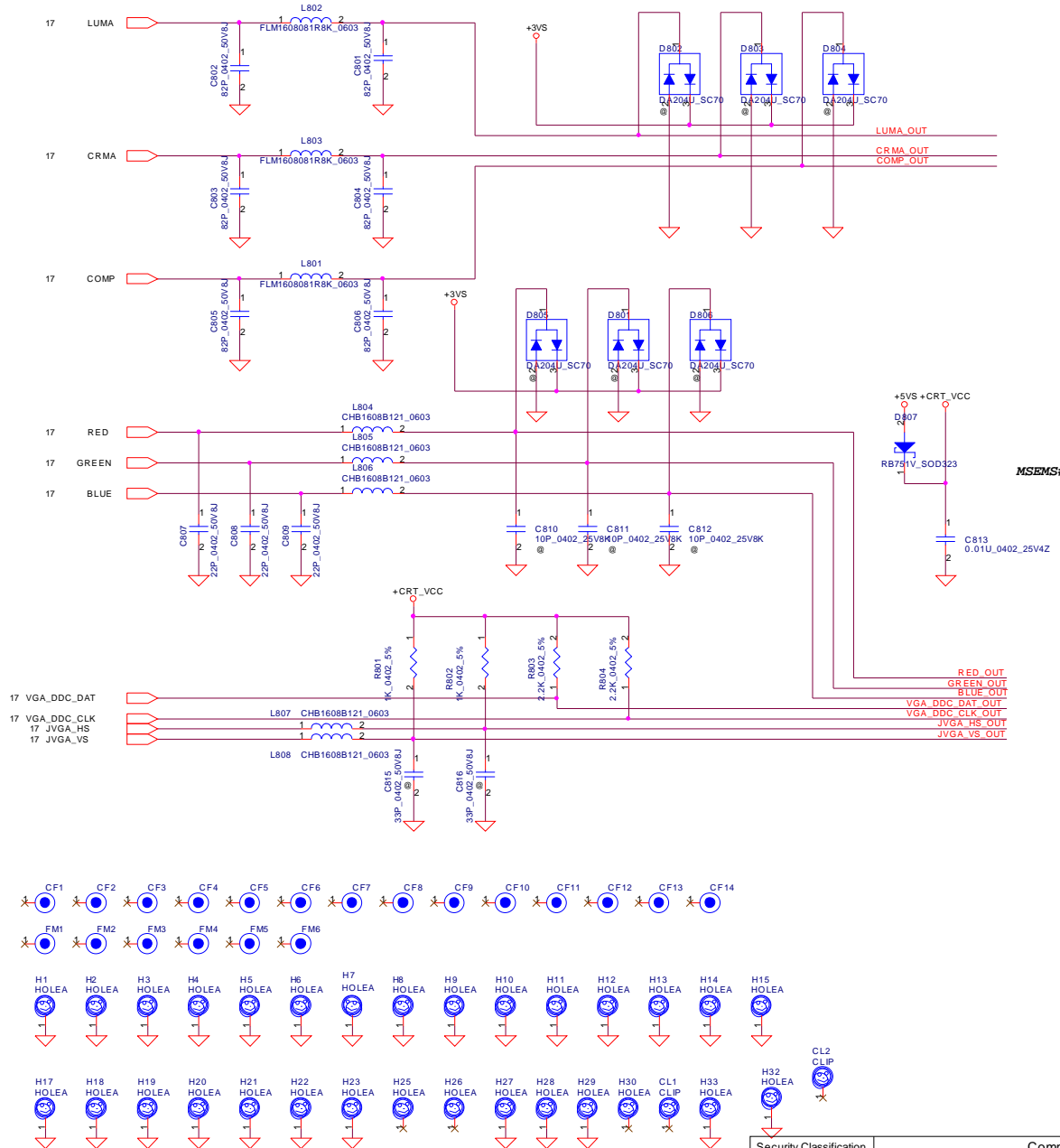


Security Classification	Compal Secret Data		Title	
Issued Date	2005/10/06	Deciphered Date	2006/10/06	BIOS & EC I/O Port
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				Document Number 1GL50/S1 LA-3771
				Rev 0.1
Date: 星期 一, 七月 31, 2006		Sheet 34 of 48		



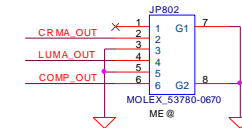
Security Classification	Compal Secret Data		Title	
Issued Date	2005/10/06	Deciphered Date	2006/10/06	DC/DC Circuit
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				Document Number
				1GL50/51 LA-3771
				Rev
				0.1
Date: 星期一, 七月 31, 2006				Sheet 35 of 48

CLOSE TO JTVOUT1

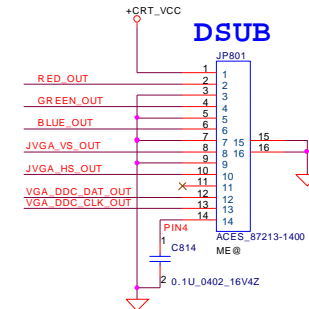


VGA I/O PORT Connector

S-VIDEO



DSUB



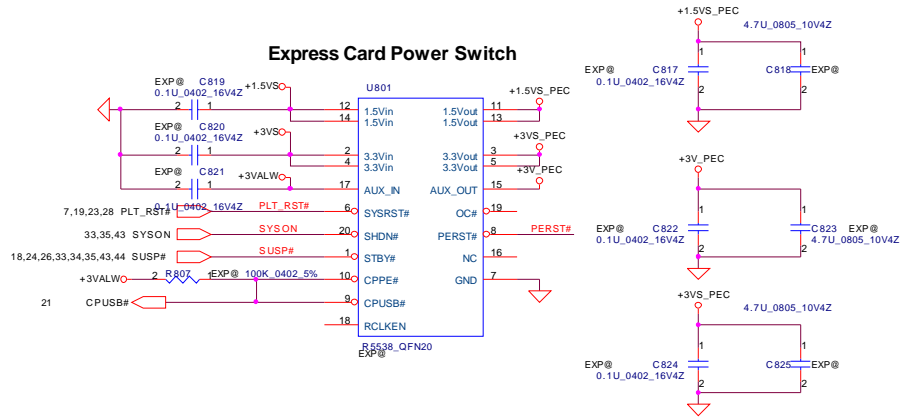
PIN ASSIGNMENT

PIN	D-SUB	FUNCTION	PIN	SVIDEO	FUNCTION
1	9	+CRT_VCC	1	1	NC
2	1	RED	2	4	CRMA
3	6	GND	3	2	GND
4	2	GREEN	4	3	LUMA
5	7	GND	5	5	GND
6	3	BLUE	6	6	CVBS
7	8	GND			
8	14	VSYNC			
9	10	GND			
10	13	HSYNC			
11	11	SENSE			
12	12	SM_DAT			
13	15	SM_CLK			
14	4	PIN4			

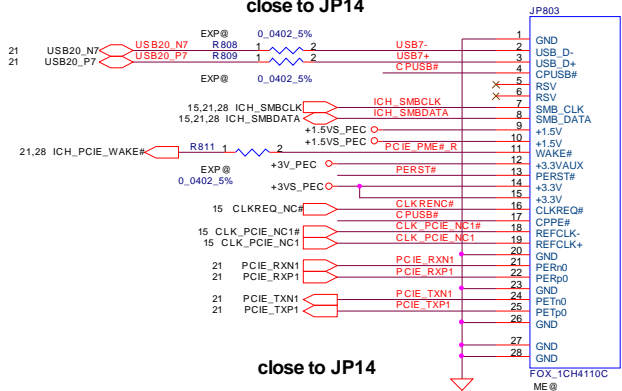
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/03/10	Deciphered Date	2006/03/10	Title	CRT & TVout Connector
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				Custom	IGL50/51 LA-3771
				Date	Rev 0.1
				Sheet	36 of 48

NEW CARD FOR C38

Express Card Power Switch



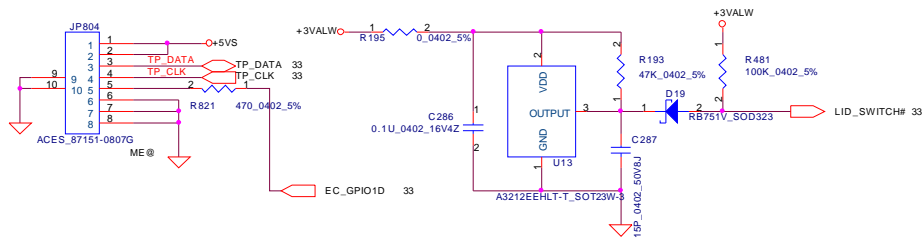
close to JP14



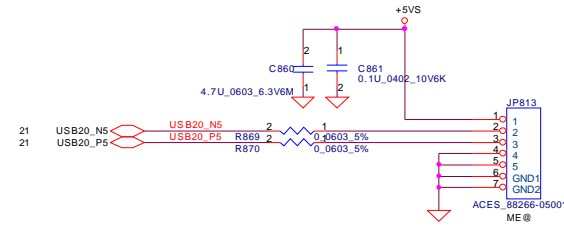
close to JP14

T/P Board

LID Switch

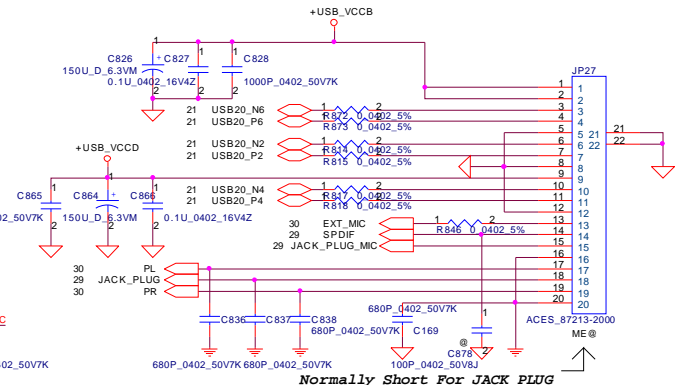
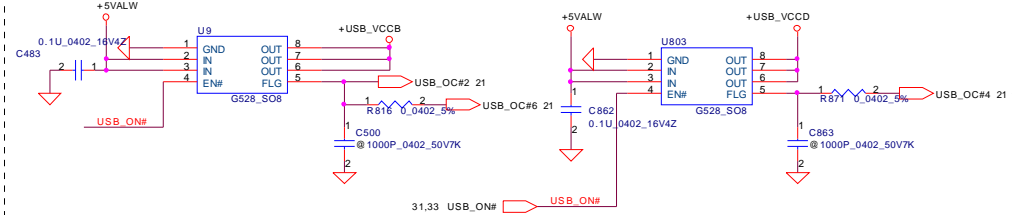


CMOS Camera Conn



USB board

Pulled up on ICH7M side



C835 ~ C838 For EMI Solution

Normally Short For JACK_PLUG

Security Classification	Compal Secret Data		Title	
Issued Date	2005/03/10	Deciphered Date	2006/03/10	INDICATE LED
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				Custom IGL50/51 LA-3771
				Date: 01.01.2006
				Sheet 37 of 48

4 in 1 Card Reader

Pinout for 4 in 1 Card Reader (JP814):

Pin	Signal	Notes
1	SD-VCC	
2	SD-MSDATA1	
3	SD-MSDATA2	
4	SD-MSDATA3	
5	SD-MSDATA0	
6	SD-MSDATA1	
7	SD-MSDATA2	
8	SD-MSDATA3	
9	SD-MSDATA0	
10	SD-MSDATA1	
11	SD-MSDATA2	
12	SD-MSDATA3	
13	SD-MSDATA0	
14	SD-MSDATA1	
15	SD-MSDATA2	
16	SD-MSDATA3	
17	SD-MSDATA0	
18	SD-MSDATA1	
19	SD-MSDATA2	
20	SD-MSDATA3	
21	SD-MSDATA0	
22	SD-MSDATA1	
23	SD-MSDATA2	
24	SD-MSDATA3	
25	SD-MSDATA0	
26	SD-MSDATA1	
27	SD-MSDATA2	
28	SD-MSDATA3	
29	SD-MSDATA0	
30	SD-MSDATA1	
31	SD-MSDATA2	
32	SD-MSDATA3	
33	SD-MSDATA0	
34	SD-MSDATA1	
35	SD-MSDATA2	
36	SD-MSDATA3	
37	SD-MSDATA0	
38	SD-MSDATA1	
39	SD-MSDATA2	
40	SD-MSDATA3	
41	SD-MSDATA0	

UMA LCD/PANEL Conn.

Pinout for UMA LCD/PANEL Conn. (JP815):

Pin	Signal	Notes
1	EDID_CLK_LCD	
2	EDID_DAT_LCD	
3	EDID_CLK_LCD	
4	EDID_DAT_LCD	
5	EDID_CLK_LCD	
6	EDID_DAT_LCD	
7	EDID_CLK_LCD	
8	EDID_DAT_LCD	
9	EDID_CLK_LCD	
10	EDID_DAT_LCD	
11	EDID_CLK_LCD	
12	EDID_DAT_LCD	
13	EDID_CLK_LCD	
14	EDID_DAT_LCD	
15	EDID_CLK_LCD	
16	EDID_DAT_LCD	
17	EDID_CLK_LCD	
18	EDID_DAT_LCD	
19	EDID_CLK_LCD	
20	EDID_DAT_LCD	
21	EDID_CLK_LCD	
22	EDID_DAT_LCD	
23	EDID_CLK_LCD	
24	EDID_DAT_LCD	
25	EDID_CLK_LCD	
26	EDID_DAT_LCD	
27	EDID_CLK_LCD	
28	EDID_DAT_LCD	
29	EDID_CLK_LCD	
30	EDID_DAT_LCD	
31	EDID_CLK_LCD	
32	EDID_DAT_LCD	

4 in 1 Card Reader

Pinout for 4 in 1 Card Reader (JP814):

Pin	Signal	Notes
1	SD-VCC	
2	SD-MSDATA1	
3	SD-MSDATA2	
4	SD-MSDATA3	
5	SD-MSDATA0	
6	SD-MSDATA1	
7	SD-MSDATA2	
8	SD-MSDATA3	
9	SD-MSDATA0	
10	SD-MSDATA1	
11	SD-MSDATA2	
12	SD-MSDATA3	
13	SD-MSDATA0	
14	SD-MSDATA1	
15	SD-MSDATA2	
16	SD-MSDATA3	
17	SD-MSDATA0	
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19	SD-MSDATA2	
20	SD-MSDATA3	
21	SD-MSDATA0	
22	SD-MSDATA1	
23	SD-MSDATA2	
24	SD-MSDATA3	
25	SD-MSDATA0	
26	SD-MSDATA1	
27	SD-MSDATA2	
28	SD-MSDATA3	
29	SD-MSDATA0	
30	SD-MSDATA1	
31	SD-MSDATA2	
32	SD-MSDATA3	
33	SD-MSDATA0	
34	SD-MSDATA1	
35	SD-MSDATA2	
36	SD-MSDATA3	
37	SD-MSDATA0	
38	SD-MSDATA1	
39	SD-MSDATA2	
40	SD-MSDATA3	
41	SD-MSDATA0	

UMA LCD/PANEL Conn.

Pinout for UMA LCD/PANEL Conn. (JP815):

Pin	Signal	Notes
1	EDID_CLK_LCD	
2	EDID_DAT_LCD	
3	EDID_CLK_LCD	
4	EDID_DAT_LCD	
5	EDID_CLK_LCD	
6	EDID_DAT_LCD	
7	EDID_CLK_LCD	
8	EDID_DAT_LCD	
9	EDID_CLK_LCD	
10	EDID_DAT_LCD	
11	EDID_CLK_LCD	
12	EDID_DAT_LCD	
13	EDID_CLK_LCD	
14	EDID_DAT_LCD	
15	EDID_CLK_LCD	
16	EDID_DAT_LCD	
17	EDID_CLK_LCD	
18	EDID_DAT_LCD	
19	EDID_CLK_LCD	
20	EDID_DAT_LCD	
21	EDID_CLK_LCD	
22	EDID_DAT_LCD	
23	EDID_CLK_LCD	
24	EDID_DAT_LCD	
25	EDID_CLK_LCD	
26	EDID_DAT_LCD	
27	EDID_CLK_LCD	
28	EDID_DAT_LCD	
29	EDID_CLK_LCD	
30	EDID_DAT_LCD	
31	EDID_CLK_LCD	
32	EDID_DAT_LCD	

RJ11+RJ45 CONN

MDC CONN

LED Indicator

Wireless / Bluetooth LED

STATUS	
AC	BLUE
Chargin	Blinking Blue
Low BATT	Amber

Function	
KEY Matrix	K016 K017
KS10	DW-UP DW-DOWN
KS11	DW-ENTER MUTE

Dial Wheel

NOVO BTN

For EMI

SUBWOOFER_ON/OFF_BTN#

RF_ON/OFF_BTN#

Blue : Power On,
Blinking Blue : Suspend

+5VALW +5VS

+VCC5_LED

NOVA_BTN#

C839, C841, C842 For EMI Solution

Left Switch BD

Right Switch BD

RJ11+RJ45 CONN

MDC CONN

LED Indicator

Wireless / Bluetooth LED

STATUS	
AC	BLUE
Chargin	Blinking Blue
Low BATT	Amber

Function	
KEY Matrix	K016 K017
KS10	DW-UP DW-DOWN
KS11	DW-ENTER MUTE

Dial Wheel

NOVO BTN

For EMI

SUBWOOFER_ON/OFF_BTN#

RF_ON/OFF_BTN#

Blue : Power On,
Blinking Blue : Suspend

+5VALW +5VS

+VCC5_LED

NOVA_BTN#

C839, C841, C842 For EMI Solution

Left Switch BD

Right Switch BD

[illegible]

RJ11+RJ45 CONN

MDC CONN

LED Indicator

Wireless / Bluetooth LED

STATUS	
AC	BLUE
Chargin	Blinking Blue
Low BATT	Amber

Function	
KEY Matrix	K016 K017
KS10	DW-UP DW-DOWN
KS11	DW-ENTER MUTE

Dial Wheel

NOVO BTN

For EMI

SUBWOOFER_ON/OFF_BTN#

RF_ON/OFF_BTN#

Blue : Power On,
Blinking Blue : Suspend

+5VALW +5VS

+VCC5_LED

NOVA_BTN#

C839, C841, C842 For EMI Solution

Left Switch BD

Right Switch BD

RJ11+RJ45 CONN

MDC CONN

LED Indicator

Wireless / Bluetooth LED

STATUS	
AC	BLUE
Chargin	Blinking Blue
Low BATT	Amber

Function	
KEY Matrix	K016 K017
KS10	DW-UP DW-DOWN
KS11	DW-ENTER MUTE

Dial Wheel

NOVO BTN

For EMI

SUBWOOFER_ON/OFF_BTN#

RF_ON/OFF_BTN#

EC_GPIO12

WIRELESS_LED#

CHARGE_LED0#

CHARGE_LED1#

BATT_LOW_LED#

PWR_LED#

Blue : Power On,
Blinking Blue : Suspend

+5VALW

+5VS

+VCC5_LED

Right Switch BD

C839, C841, C842 For EMI Solution

Security Classification	Compal Secret Data			Title	
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RJ11+RJ45 CONN

MDC CONN

LED Indicator

Wireless / Bluetooth LED

BATT_CHG_LED#

BATT_LOW_LED#

STATUS	
AC	BLUE
Chargin	Blinking Blue
Low BATT	Amber

Function

KEY Matrix	K016	K017
KS10	DW-UP	DW-DOWN
KS11	DW-ENTER	MUTE

Dial Wheel

NOVO BTN

For EMI

SUBWOOFER_ON/OFF_BTN#

RF_ON/OFF_BTN#

EC_GPIO12

WIRELESS_LED#

CHARGE_LED0#

CHARGE_LED1#

PWR_LED#

Blue : Power On,
Blinking Blue : Suspend

C839, C841, C842 For EMI Solution

Security Classification	Compal Secret Data		Title	
Issued Date	2005/03/10	Deciphered Date	2006/03/10	INDICATE LED
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RJ11+RJ45 CONN

MDC CONN

LED Indicator

Wireless / Bluetooth LED

BATT_CHG_LED#

BATT_LOW_LED#

STATUS	
AC	BLUE
Chargin	Blinking Blue
Low BATT	Amber

Function

KEY Matrix	K016	K017
K010	DW-UP	DW-DOWN
K011	DW-ENTER	MUTE

Dial Wheel

NOVO BTN

For EMI

SUBWOOFER_ON/OFF_BTN#

RF_ON/OFF_BTN#

33 EC_GPIO12
33 EC_GPIO19
28 WIRELESS_LED#
28 BTONLED

33 CHARGE_LED0#
33 CHARGE_LED1#
33 PWR_LED#

Blue : Power On,
Blinking Blue : Suspend

C839, C841, C842 For EMI Solution

Security Classification	Compal Secret Data		Title	
Issued Date	2005/03/10	Deciphered Date	2006/03/10	INDICATE LED
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RJ11+RJ45 CONN

LED Indicator

Wireless / Bluetooth LED

Left Switch BD

Right Switch BD

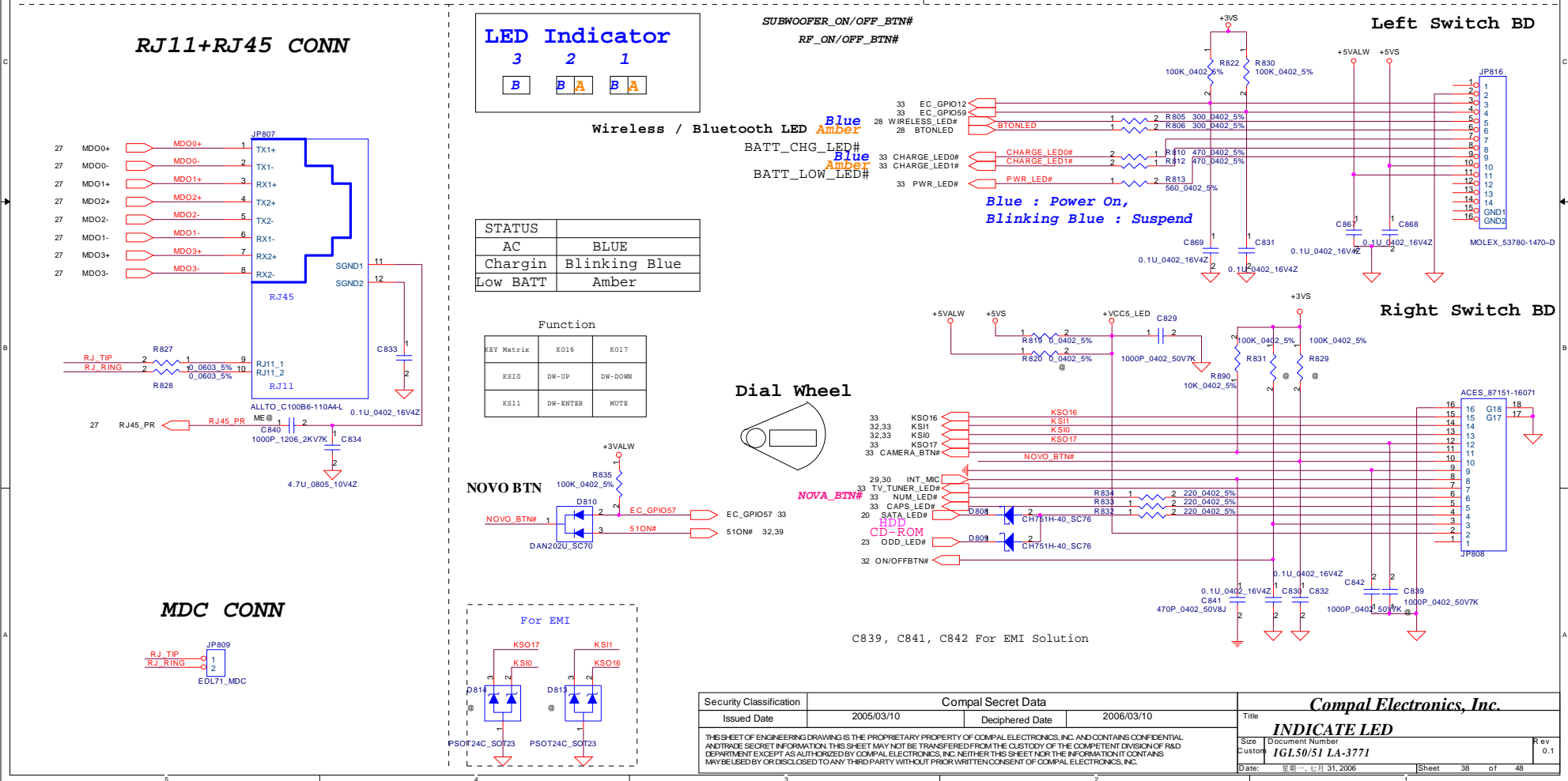
MDC CONN

NOVO BTN

For EMI

Security Classification

Security Classification	Compal Secret Data		Title	
Issued Date	2005/03/10	Deciphered Date	2006/03/10	INDICATE LED
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RJ11+RJ45 CONN

LED Indicator

Wireless / Bluetooth LED

Left Switch BD

Right Switch BD

MDC CONN

NOVO BTN

Dial Wheel

For EMI

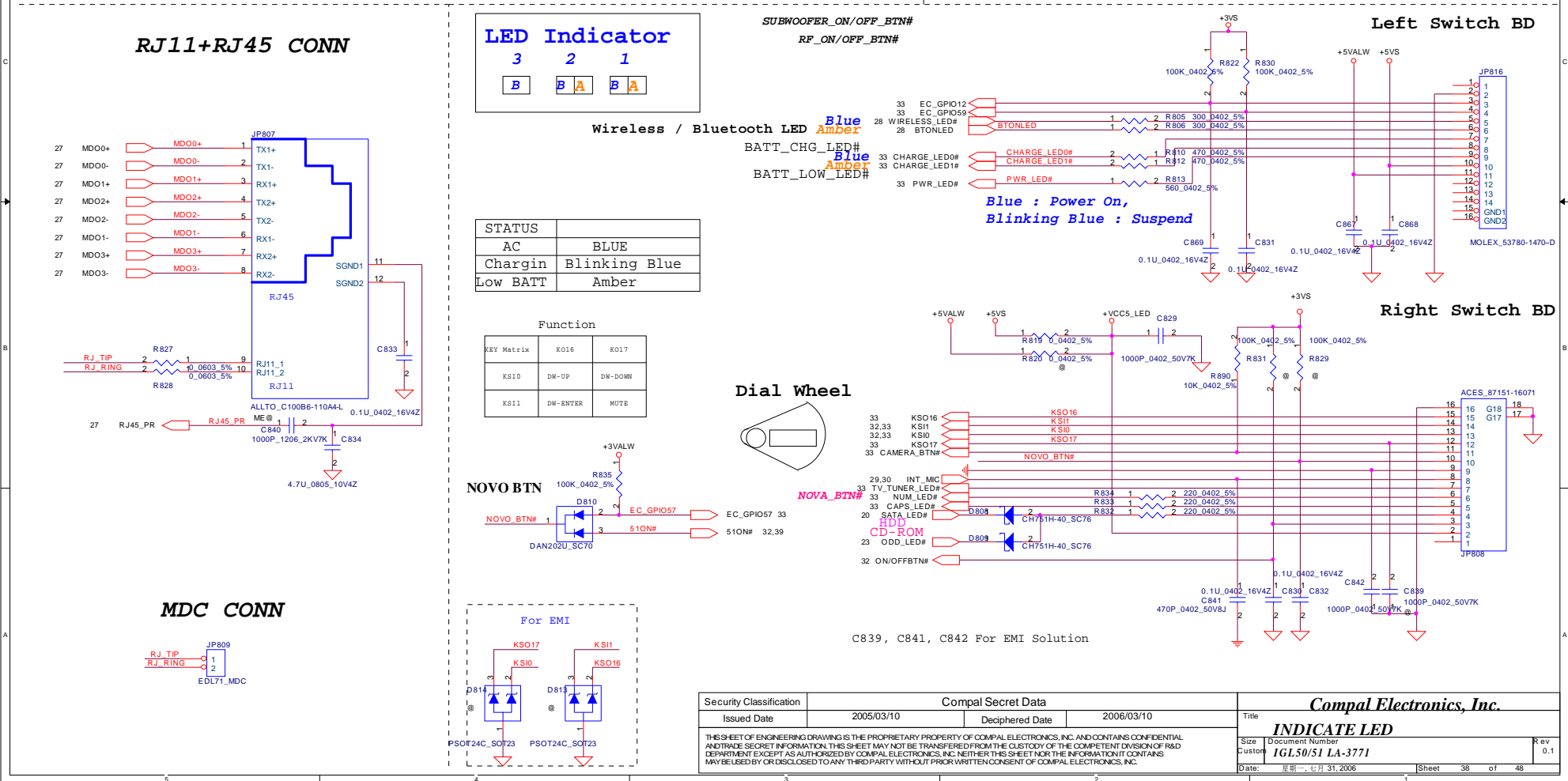
Security Classification

Security Classification	Compal Secret Data	Title
Issued Date	2005/03/10	Deciphered Date
2006/03/10		

INDICATE LED

1G4.50/51 LA-3771

Rev 0.1



RJ11+RJ45 CONN

LED Indicator

Wireless / Bluetooth LED

Left Switch BD

Right Switch BD

MDC CONN

NOVO BTN

Dial Wheel

For EMI

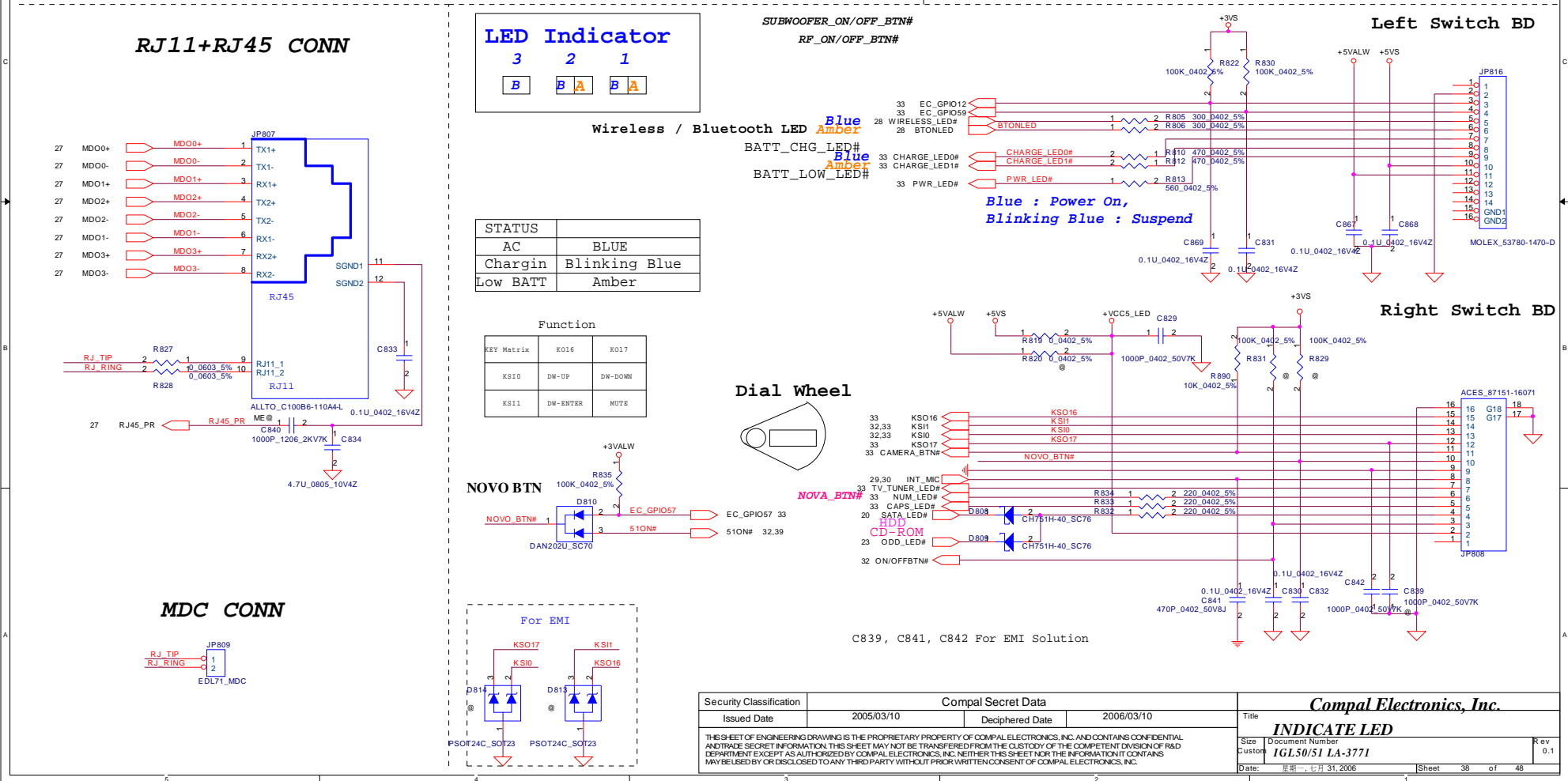
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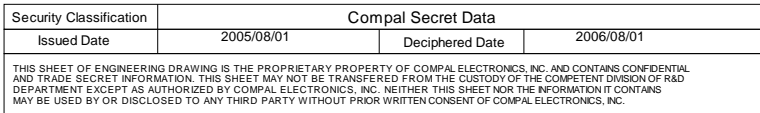
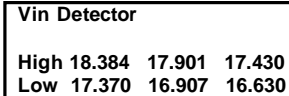
Security Classification	Compal Secret Data	Title
Issued Date	2005/03/10	Deciphered Date
2006/03/10		

INDICATE LED

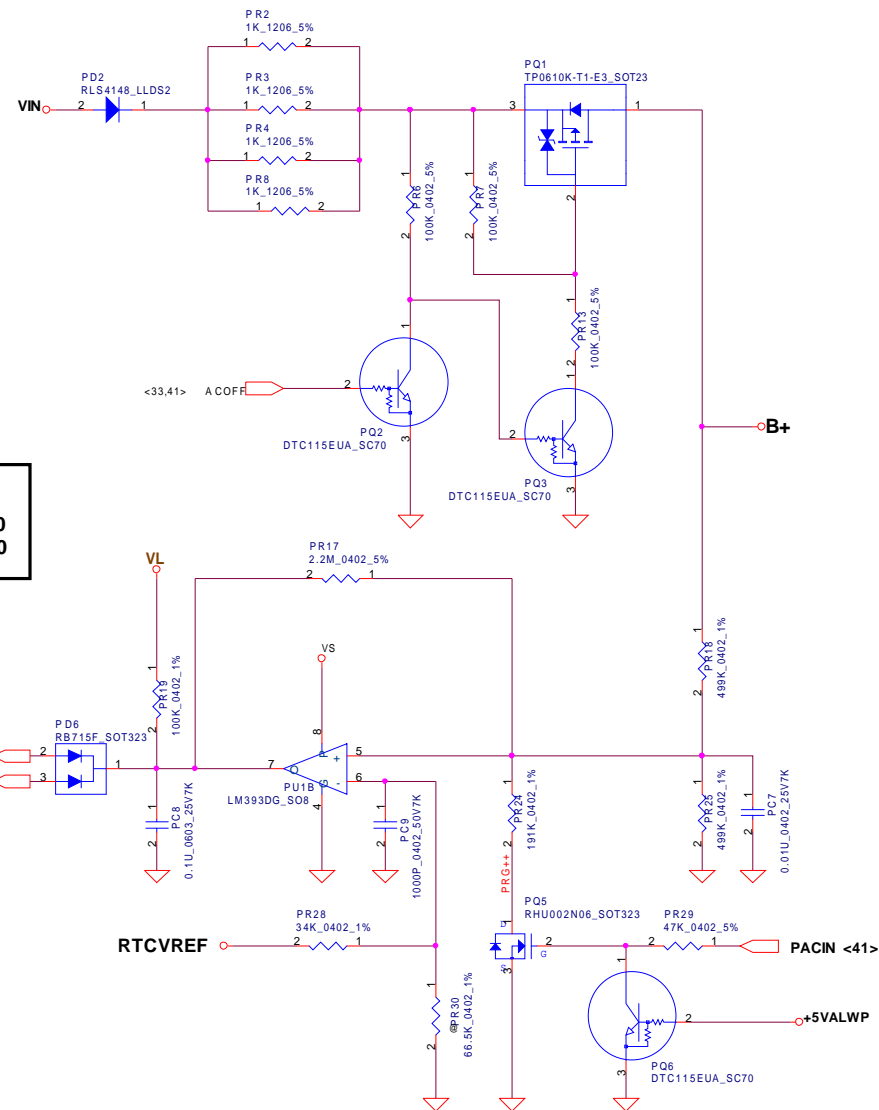
1G4.50/51 LA-3771

Rev 0.1

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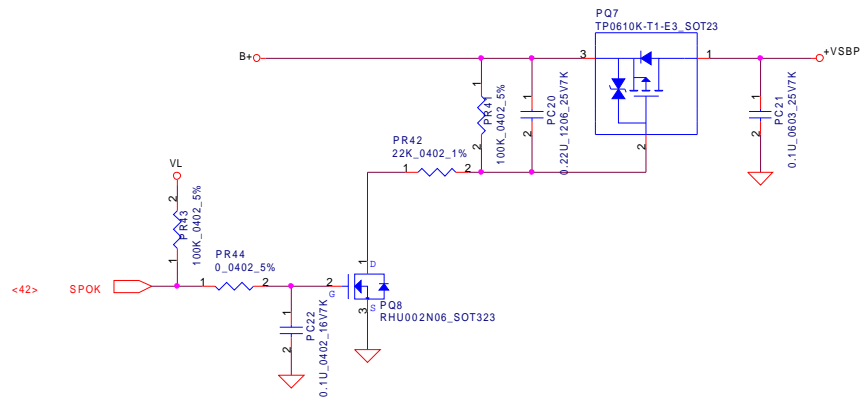
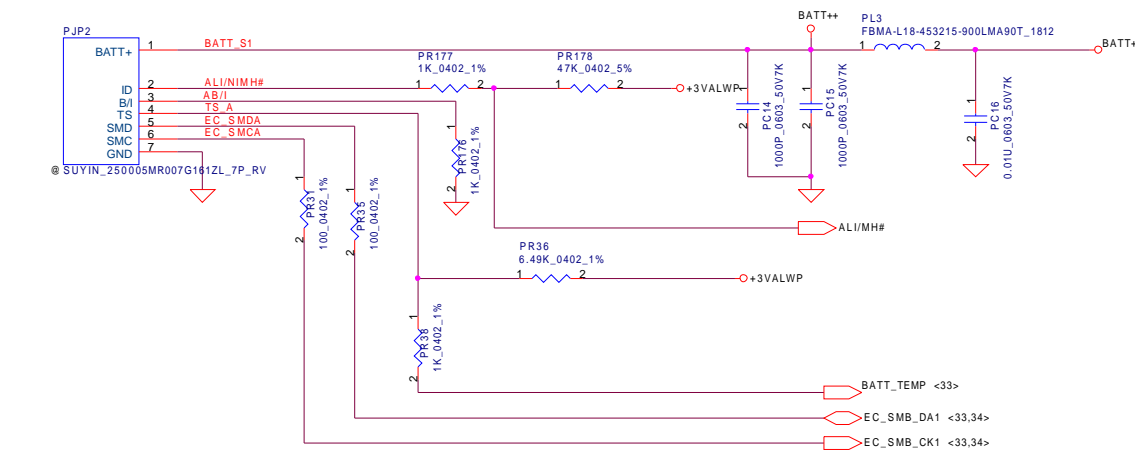


BATT ONLY			
	Precharge detector		
	Min.	typ.	Max.
H->L	6.169V	6.231V	6.361V
L->H	7.168V	7.349V	7.537V

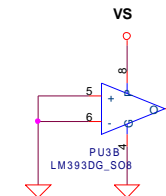
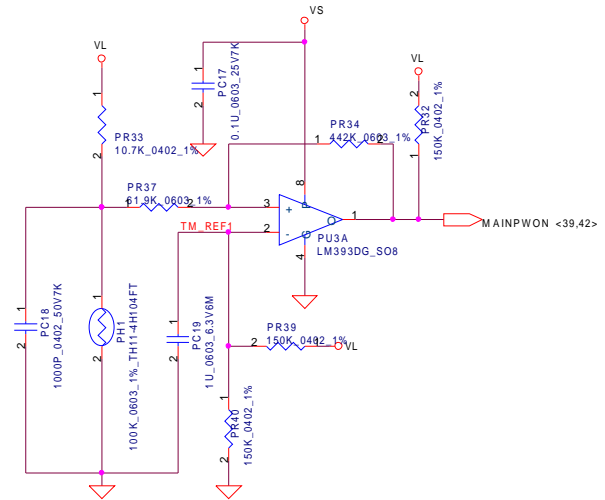


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Title		
DCIN/DECTOR		
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PH1 under CPU botten side :
CPU thermal protection at 85 degree C
Recovery at 70 degree C

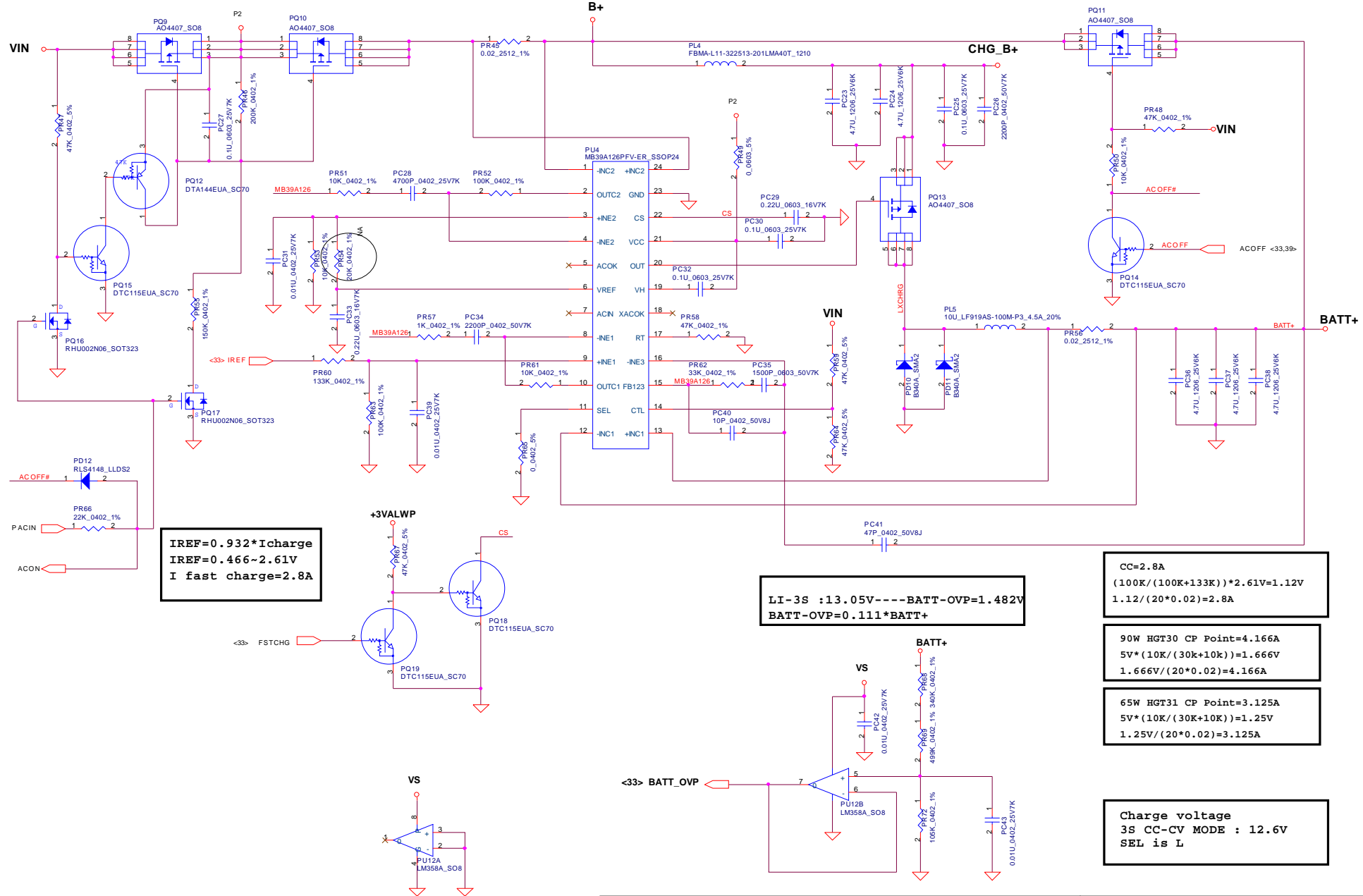


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				BATTERY CONN. / OTP				
				Size B	Document Number		Rev 0.1	
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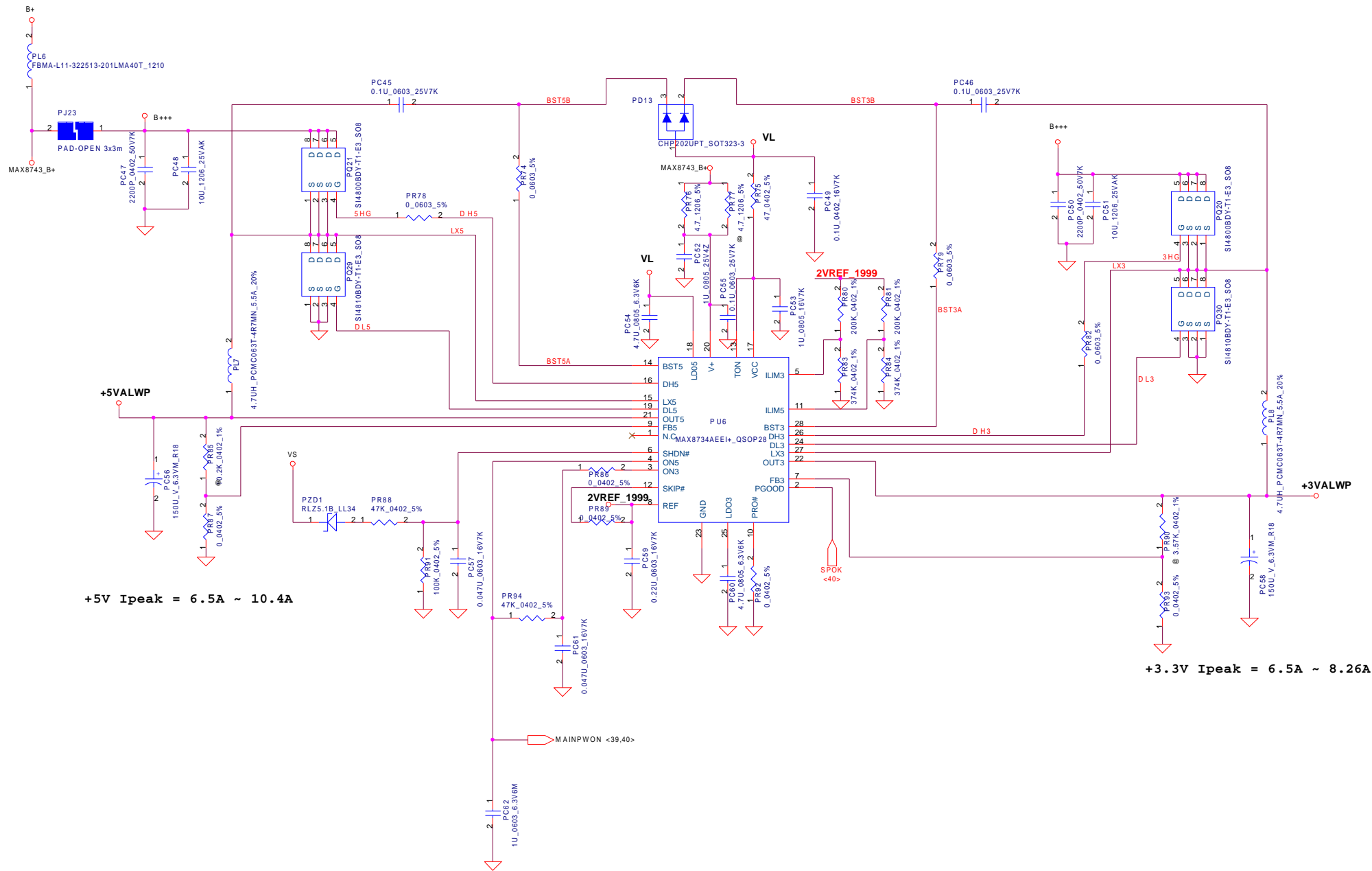
65W PR45=0.02_2512_1% PR54=30K_0402_1% Iadp=0~3.125A
90W PR45=0.02_2512_1% PR54=20K_0402_1% Iadp=0~4.166A

Fosc=14100/Rt=14100/47=300KHz

Charger

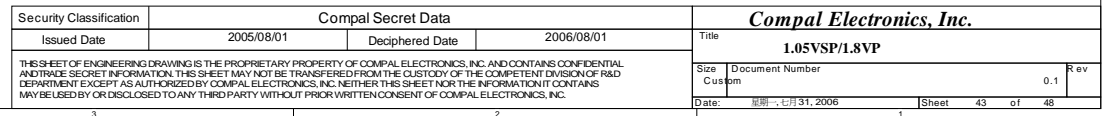


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Size	B	Document Number		Rev	0.1
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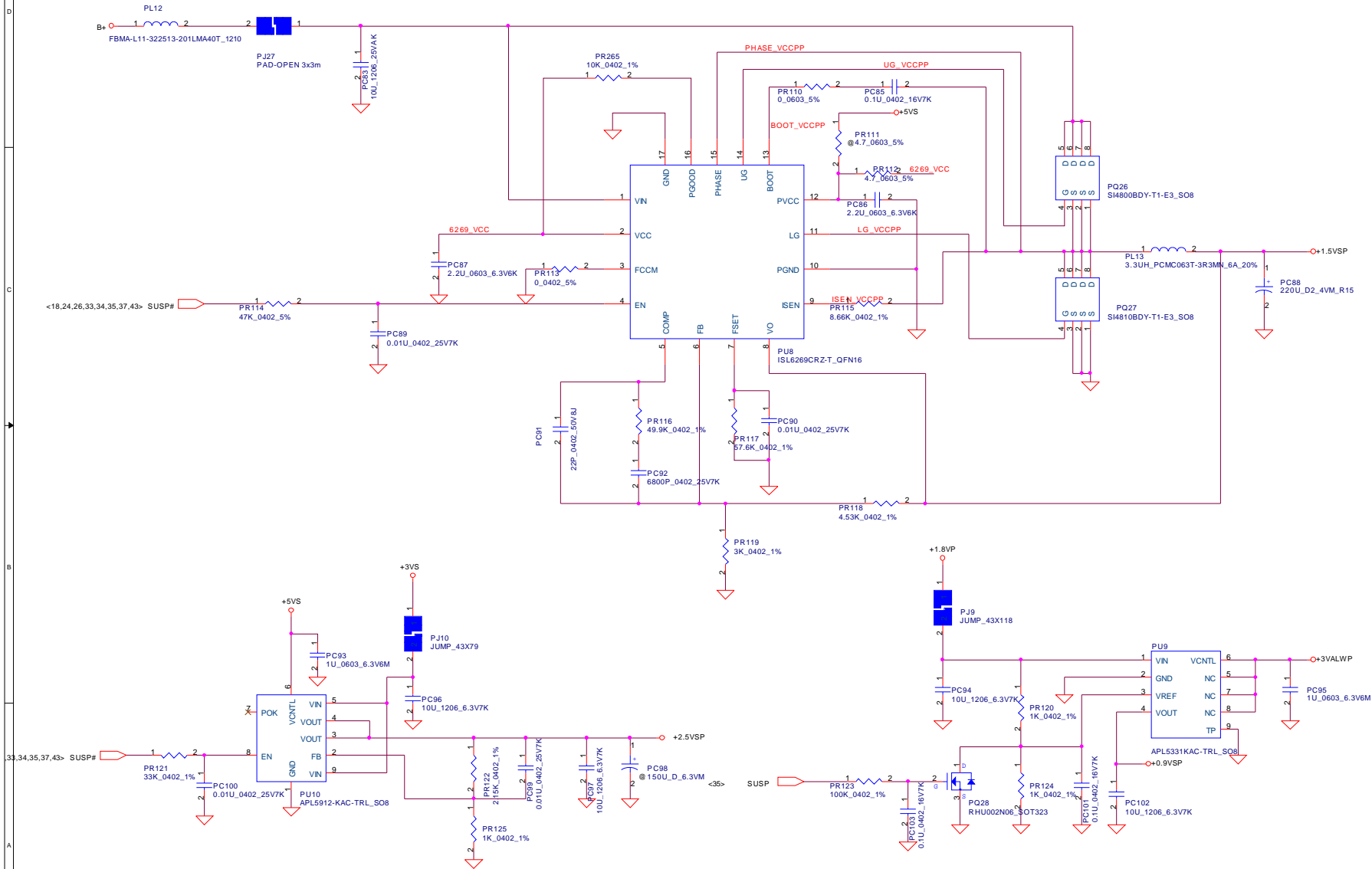


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Issued Date	2005/08/01	Deciphered Date	2006/08/01	Title	
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Size	Document Number	Rev		Date	
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+1.05VSP Ilimit=6.33A~10.03A



+1.5VSP Ilimit = 9.38A~12.5A



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Issued Date	2005/08/01	Deciphered Date	2006/08/01	Title +VCCPP/+2.5VSP/0.9VSP	
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Version change list (P.I.R. List)

Page 1 of 1
for PWR

Item	Fixed Issue	Reason for change	Rev.	PG#	Modify List	VER	Phase
1		MODIFY 3V/5V current limit to 6.5A~8.1A/6.5A to 10.2A		42	MODIFY PR83/PR84 FROM 499K TO 374K		DVT
2		ADD or decrease CPU CORE ring with EMI solution : snubber		45	Reserve PR224//PR256: 4.7 1206 ,add PC167/PC182:680P		DVT
3		Reserve PR267,PR268 seperate in CPU CORE high side gate for EMI require		45	Reserve PR267,PR268:0 0603		DVT
4		change PJP1 from 5 pin to 4 pin		39	change PJP1 from 5 pin to 4 pin		DVT
5		modify sequecce		43	change PR179 to 100k, PC132 =0.1U		DVT
6		modify Vgate		45	add PQ38:RHU002N06,PR240:2K,delete PR247		
7		Prevent noise interference cpu_core feedback		45	Reserve PC185, PC186, PC187, PC188		
8							
9							
10							
11							
8							
9							

Compal Electronics, Inc.

Title

PIR (PWR)

Size

Document Number

Rev
0.2

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Version change list (P.I.R. List)

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Item	Fixed Issue	Rev.	PG#	Modify List	B.Ver#	Phase
1	PC Beep modify	0.2	29	Modify C714 from 0603 to 0402	0.2	DVT
2	Head Phone no sense	0.2	29	Add R892 for Audio channel B use	0.2	DVT
3	PC BEEP FOR DOS MODE	0.2	29	Change 1u to 680p	0.2	DVT
4	CIR for ALW power	0.2	32	Add R893 for CIR	0.2	DVT
5	To prevent voltage feedback to +3VS from KB_RST#	0.2	33	Add D812	0.2	DVT
6	EAPD change to AC_RST_AUDIO#	0.2	29	Change R458	0.2	DVT
7					0.2	DVT
					0.2	DVT
					0.2	DVT
8					0.2	DVT
9					0.2	DVT
10					0.2	DVT
					0.2	DVT
11					0.2	DVT

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Compal Electronics, Inc.		
Title		
PIR		
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	HGT30/31 LA-3061	0.0
Date:	星期日, 七月 31, 2006	Sheet 47 of 48

Version change list (P.I.R. List)

Item	Fixed Issue	Rev.	PG#	Modify List	B.Ver#	Phase
12					0.3	PVT
13					0.3	PVT
14					0.3	PVT
15					0.3	PVT
16					0.3	PVT

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Compal Electronics, Inc.		
Title PIR		
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Date	星期 日, 七月 31, 2006	
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